

Using Constant Switching Frequency in a Linear-Assisted DC-DC Switching Regulator

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Abstract—This article shows the proposal of a linear-assisted converter or linear-&-switching hybrid converter with a constant switching frequency. The control loop of the system is based on the current-mode technique. The main disadvantage of a converter with current-mode control is the inherent instability of the loop when switch duty ratios are greater than 0.5. In order to make stable the proposed linear-assisted converter, the article shows the technique based on a slope compensation.

Keywords—DC-DC switching converters; voltage linear regulators; linear-assisted DC-DC voltage regulators

I. INTRODUCTION (HEADING 1)

As it is well known, series *linear regulators* have been structures widely used in power supply systems for decades, providing output voltages with low or moderate consumes and currents [1], [2]. This kind of voltage regulators has several advantages that lead their use. However, in spite of these advantages, linear regulators suffer from some serious drawbacks. One significant example is that the efficiency of these structures hardly exceeds the 50% and the series-pass transistor has to support all the current demanded by the load. Thus, in high power applications, this component has to be dimensioned (both electrically and thermally) in order to dissipate powers with high values. This point increases the price of the supply system significantly. As a result, they are not recommendable in some supply systems, especially for high power.

The alternative is also widely known: *DC-DC switching converters* [3]–[5]. The advantages of these structures are also evident opposite to linear regulators: the efficiency of them, in spite of not arriving at the 100% due to the omnipresent circuit losses, is near to this optimal value. However, the design and implementation of this sort of converters is a more complex process than in linear regulators, especially their control loops when both line and load regulations are desired. In addition, the intrinsic switched nature of these converters produces ripples in the output voltage and an increment of the EMIs in neighboring electronic systems.

Last years, a third possibility has been presented: *linear-&-switching hybrid converters* or *linear-assisted converters* [6], [7]. These converters are compact structures, which make good use of the advantages from previous alternatives (linear

regulators and switching converters). With them, some of the aforementioned drawbacks are minimized as, for instance, the low efficiency and the high power dissipation in linear regulators, or the complexity in the design of the control for switching converters.

In general, linear-assisted converters presented in literature are *variable-frequency structures* [8]–[10]. In this case, the switching frequency depends on the parameters of the converter and is not fixed by an external clock. This drawback affects the design of the converter and the selection of its components. In addition, this feature is not desirable in some applications because variable switching frequency could insert into the bandwidth of the neighboring electronic circuits or even of the electronic system that the converter supplies. Thus, with the design of a *constant-frequency linear-assisted converter*, it is possible to choose the desired switching frequency independently of the component values.

In this article, a proposal of constant-frequency linear-assisted converter, especially suitable as DC-DC regulator for photovoltaic solar-powered facilities, is presented. Section II presents a review of variable-frequency linear-assisted converters, section III and IV show the proposal of the constant-frequency linear-assisted converter, revealing the problem of its stability and the solution adopted to avoid it. Finally, section V concludes with some simulation results that validate the proposed structure.

II. REVIEW OF VARIABLE-FREQUENCY LINEAR-ASSISTED CONVERTERS

As we can see in figure 1, the proposed configuration makes use of an analog comparator that controls the conduction or cut of the transistor Q_1 and fixes the switching frequency. Note that the objective of the switching converter is to provide the excess of current that the linear regulator does not supply. Consequently:

$$I_{out} = i_{reg}(t) + i_L(t) \quad (1)$$

Just not considering in a first approximation any hysteresis in the comparator CMP_1 , the operation of the circuit is just as it is explained as follows: if the load current is below a boundary current value, that we name as *threshold switching current*, I_T , the output of the comparator CMP_1 is held low. Thus, the

switching converter will be disabled and, therefore, the current through the inductance L_1 will be zero. As a result, the linear

regulator supplies the load R_L , providing all the output current ($I_{reg}=I_{out}$).

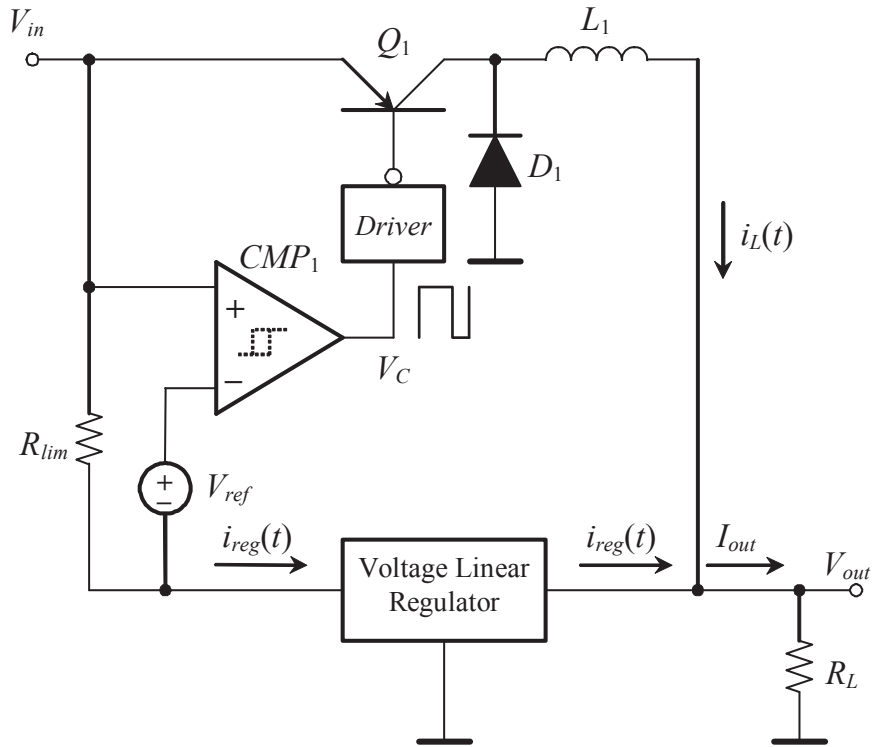


Fig. 1. Basic structure of a self-switched hybrid converter that supplies a load R_L .

However, in the moment that load current increases and goes slightly beyond this limit current I_γ , the comparator output will pass to high level, increasing the inductance current in a linear form. Taking into account expression (1) and that the load current is constant (equal to V_{out}/R_L), the regulator current $i_{reg}(t)$ will tend to decrease also linearly (figure 2), just reaching a value below I_γ . In this moment, the comparator changes its output from high to low level, the transistor Q_1 goes to off and forces the inductance current to decrease. Then, when the current in the inductance decreases to a value in which $i_{reg}(t) > I_\gamma$, the comparator changes from low to high level, and the cycle starts again.

Both reference voltage V_{ref} and shunt resistor R_{lim} determine the value of the threshold switching current I_γ . Fixing I_γ , the value of the R_{lim} can be obtained by expression (2).

$$R_{lim} = \frac{V_{ref}}{I_\gamma} \quad (2)$$

With the objective of not decreasing the efficiency of the converter, the value of the dissipated power by the internal transistor of the linear regulator must be reduced to the utmost. For this reason, the current I_γ has to be the minimum and necessary value to make the linear regulator work properly, without penalizing its good regulation characteristics.

The delays of the electronic circuits determine a little hysteresis that limits the maximum switching frequency. Nevertheless, with the objective of fixing this maximum frequency to a suitable value (in order not to increase the

switching losses significantly), it is convenient to add a hysteresis to the comparator CMP_1 .

The experimental implementation of the self-switched hybrid regulator presented in figure 1 can be found in [9] and [10]. Figure 3 shows the waveforms of a variable-frequency linear-assisted converter with $V_{in}=10\text{ V}$. In the same figure, it is observed the response of the system to an input voltage step from 10 V to 13 V in the time instant $t=20\text{ }\mu\text{s}$, and a step change of the load resistor of the 50% (from $10\text{ }\Omega$ to $5\text{ }\Omega$) in $t=40\text{ }\mu\text{s}$.

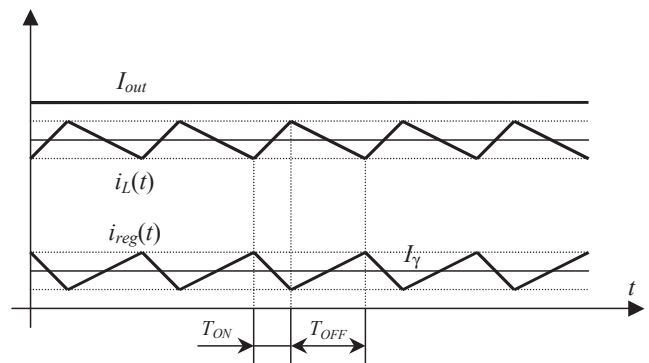


Fig. 2. Currents through the load resistor, inductance L_1 and linear regulator in the steady state.

The switching frequency is given by (3), being the upper and lower switching threshold levels of the comparator (Schmitt trigger) V_H and V_L , respectively.

$$f = \frac{R_{lim}}{L_1} \frac{V_{out}}{V_H - V_L} \left(1 - \frac{V_{out}}{V_{in}} \right) \quad (3)$$

From equation (3) we obtain the switching frequency is a function of the hysteresis of the comparator, input and output voltages and R_{lim} and L_1 values. This dependency from the

aforementioned values is an important drawback. Note that the switching frequency depends on the possible disturbances in V_{in} and tolerances and dispersions in passive components of the converter.

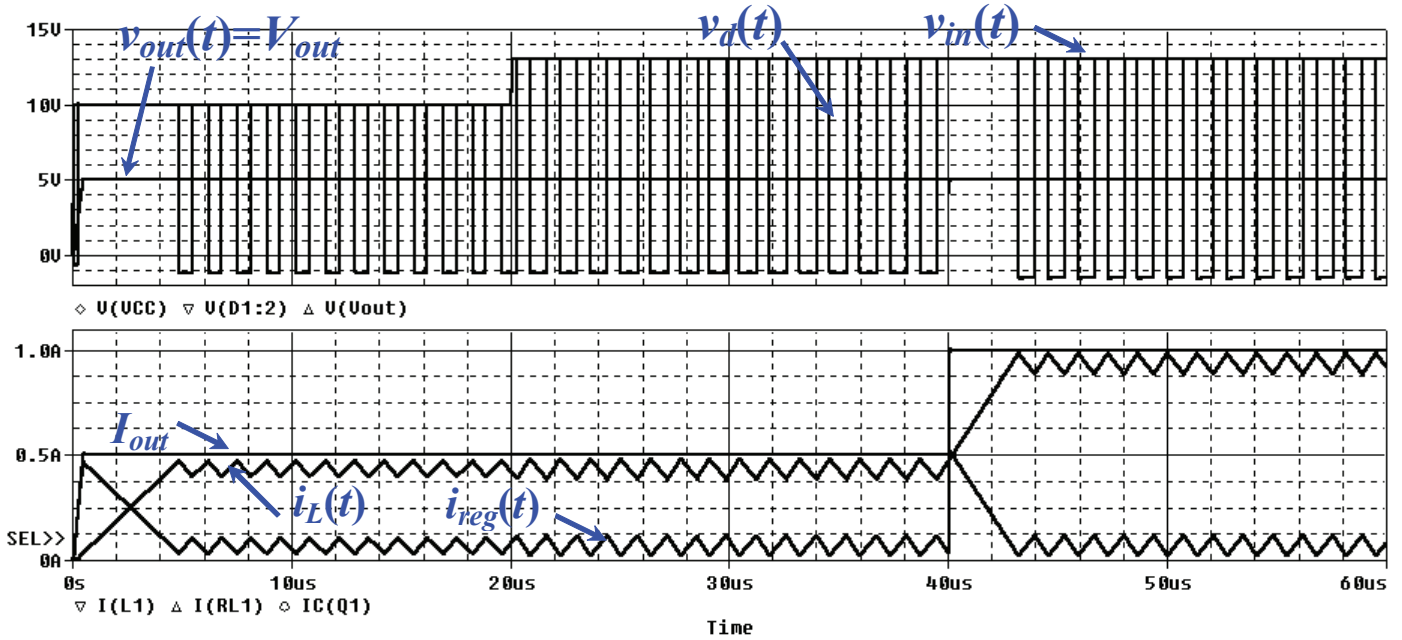


Fig. 3. Transient response of the variable–frequency linear–assisted converter with $V_{in}=10\text{ V}$. It can be seen the response of the circuit to an input step from 10 V to 13 V in the time instant $t=20\text{ }\mu\text{s}$, and when there is a decrement of the load resistance from $10\text{ }\Omega$ to $5\text{ }\Omega$ in $t=40\text{ }\mu\text{s}$.

III. CONSTANT–FREQUENCY LINEAR–ASSISTED CONVERTER

Figure 4 shows a constant–frequency linear–assisted converter. As the variable–frequency converter shown in figure 1, this topology has a switching converter and a linear

regulator. However, now the switching frequency is fixed by the external clock signal V_{CLK} . This signal is applied to the set input of a RS flip–flop and the output of the comparator is applied to the reset input of the bistable. The output of the flip–flop is applied to the base or gate of the switch Q_1 .

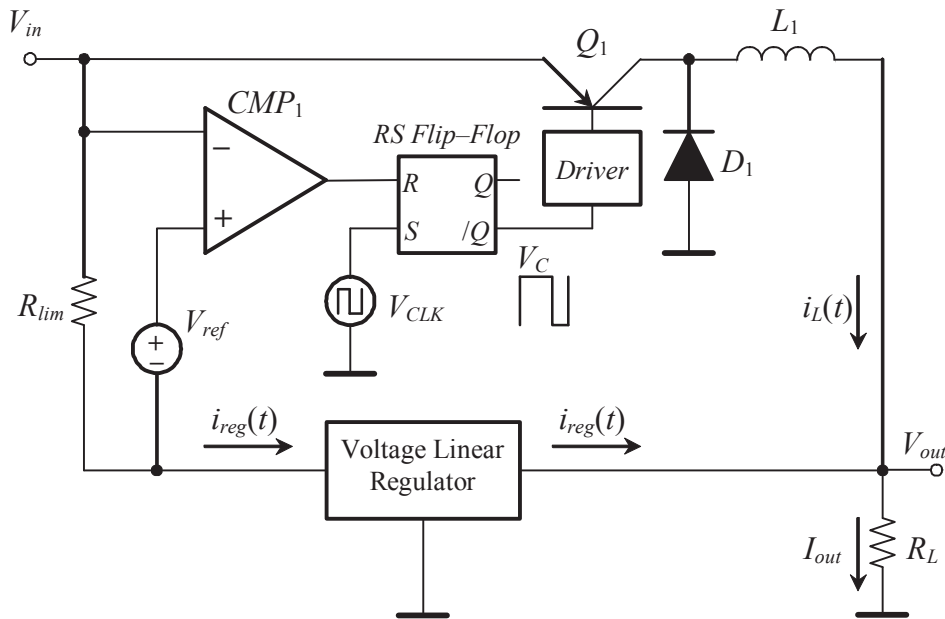


Fig. 4. Schematic of the constant–frequency linear–assisted converter that has been used to validate the proposed structure.

Figure 5 shows the waveforms of the constant-frequency linear-assisted converter with $V_{in}=15\text{ V}$. In the same figure, it is observed the response to an input voltage step from 15 V to 18 V in the time instant $t=400\ \mu\text{s}$, and a step change of the load resistor of the 50% (also from $10\ \Omega$ to $5\ \Omega$) in $t=800\ \mu\text{s}$. The output voltage is fixed, thanks to the linear regulator, to 7 V and the current I_γ has been adjusted in this case to 100 mA .

The clock signal determines the start of T_{ON} interval and the output of the comparator determines the beginning of T_{OFF} .

The current sensing of the linear regulator acts as a control loop and establishes the switch duty ratio in a similar way that the well-known PWM modulation. Figure 6 shows in detail the waveforms of the converter in the steady state.

Actually, note that in this converter the control is a current-mode control. With regard to the classical current-mode control, in this converter variations of the inductor current are measured indirectly thanks to the measure of the variations in the linear regulator.

As it is well known, the current-mode control has an important limitation: for switch duty ratios greater than 0.5 the system is unstable [5]. To prevent this limitation in the current-mode control, a slope compensation has to be added to the

control loop in order to provide stability, to prevent subharmonic oscillations, and to provide a feedforward property.

Figure 7 shows the constant-frequency linear-assisted converter with a system that implements this slope compensation by means of an integrator with reset.

IV. CONCLUSIONS

This paper shows the proposal of a linear-assisted converter or linear-&-switching hybrid converter with constant switching frequency. With regard to the linear-assisted converters with variable switching frequency, the presented structure eliminates the disadvantages derived from having variable switching frequency.

The introduced control loop is based on the current-mode technique that has as a main drawback, as usual in DC-DC converters, the instability of the loop when switch duty ratios are greater than 0.5. This work has introduced the technique based on slope compensation in order to make stable the linear-assisted converter. The proposed modifications guarantee the stability and make possible this new mode of work in this kind of converters.

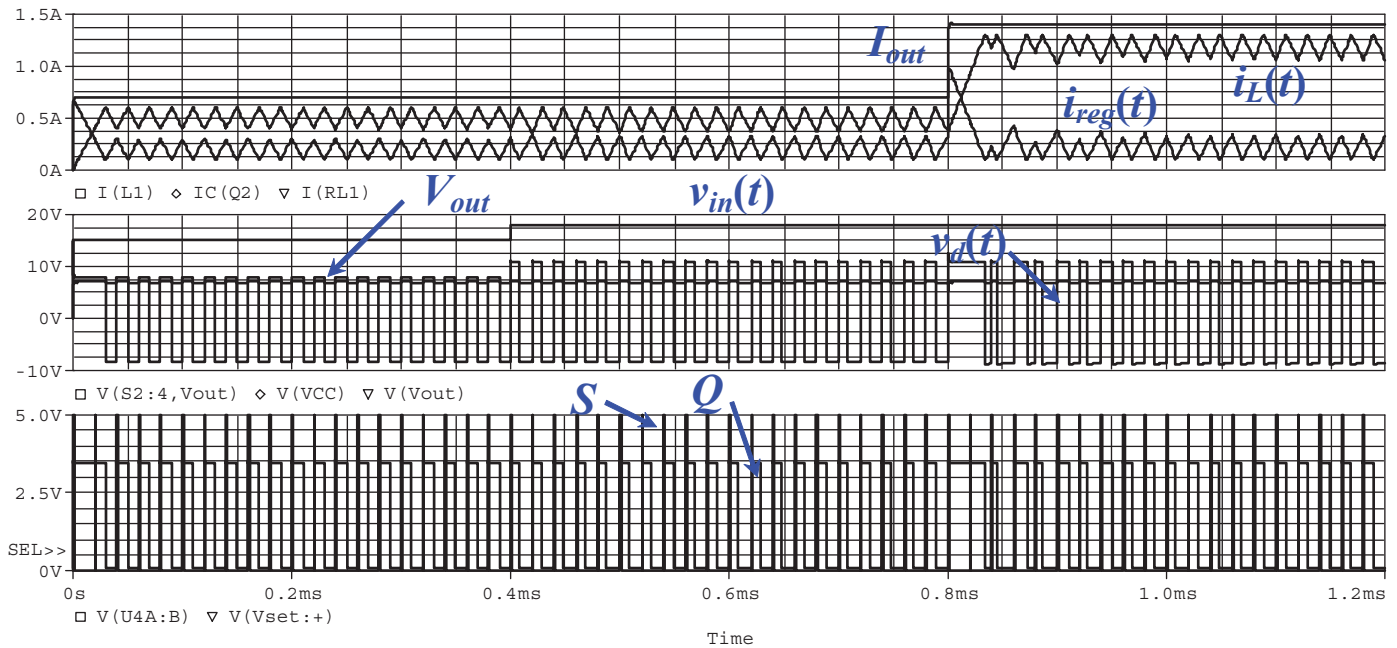


Fig. 5. Transient response of the constant-frequency linear-assisted converter with $V_{in}=15\text{ V}$. It can be seen the response of the circuit to an input step from 15 V to 18 V in the time instant $t=400\ \mu\text{s}$, and when there is a decrement of the load resistance from $10\ \Omega$ to $5\ \Omega$ in $t=800\ \mu\text{s}$.

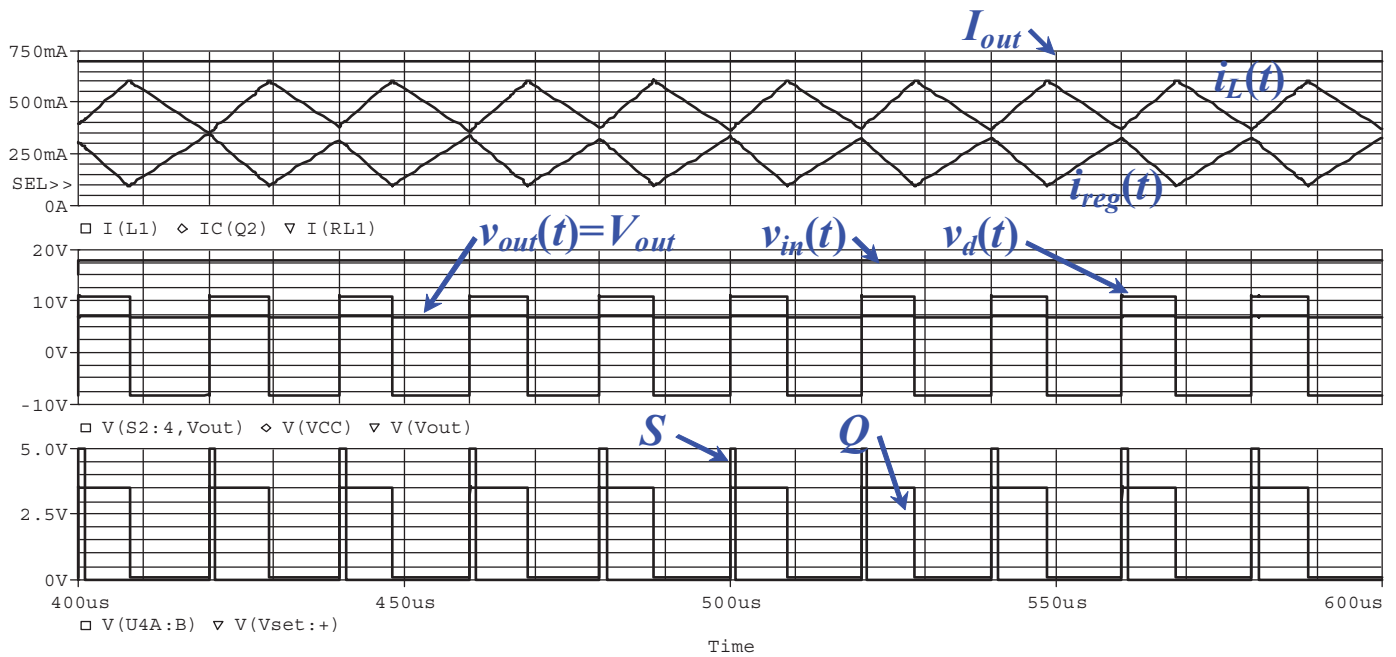


Fig. 6. Detail of the waveforms of the constant-frequency linear-assisted converter in the steady state.

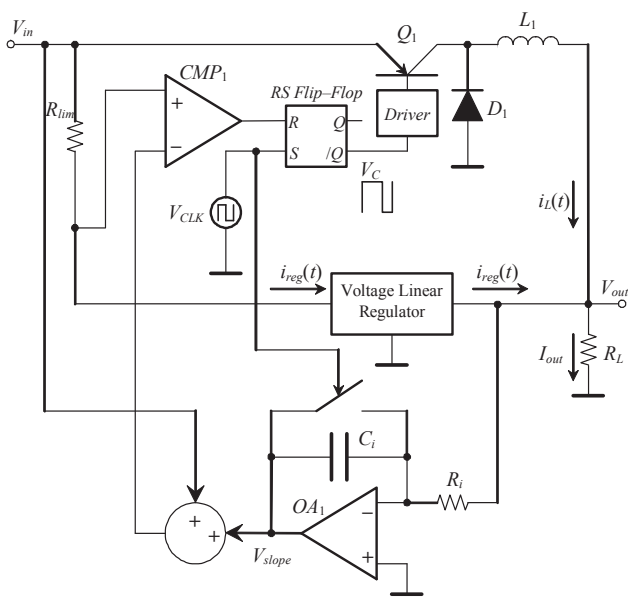


Fig. 7. Schematic of the constant-frequency linear-assisted converter with the implementation of the slope compensation.

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