Distributed Direct Power Sliding-Mode Control for Islanded AC Microgrids

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Abstract — The aim of this paper is to develop a novel distributed direct power sliding-mode control for an islanded AC microgrid. This solution replaces the droop mechanism of each inverter with two separate sliding surfaces working as primary/secondary controllers. The design of these surfaces is based on the dynamic model of the active and reactive powers to enhance the robustness against line impedance mismatches. Moreover, a theoretical stability analysis is presented. The main features of this proposed control are: first, regulate the voltage and frequency achieving an accurate active and reactive power sharing; second, achieve stability under a wide range of line impedances; and third provide robustness toward external disturbances, including communication failures. Finally, the experimental results verify the controller performance, the stability with different line impedances, and the robustness against communication partitions.

Index Terms—AC microgrid, distributed control, Slidingmode control, voltage and frequency regulation.

I. INTRODUCTION

M ICROGRIDS are small power systems that integrate sources, loads, and storages devices using fast acting power inverters. They are attracting more and more interest due to their capability of integrating different kinds of energy resources, as well as global and local loads. They can operate both connected to the main grid or in islanded mode [1], [2].In islanded AC microgrid, a three-layer hierarchical control structure is widely used [3]–[8].

In microgrid control, there exist three main control architectures which are referred to as decentralized, centralized, and distributed. The decentralized control strategies are the most widely accepted due to their reliability and simplicity [9]–[11]. In these approaches, each inverter uses

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Hasan Komurcugil is with the Computer Engineering Department, Eastern Mediterranean University, 99628 Famagusta, Turkey (email:hasan.komurcugil@emu.edu.tr). local measurement to implement active and reactive power sharing control. Although the controller does not require communication, these approaches suffer from load dependent frequency/voltage deviations [12]. Moreover, there are some limitations for power sharing in decentralized controllers since it depends on the line impedance [8]. These problems are usually compensated by additional controllers that require communications. In centralized approaches, the active and reactive power sharing control is usually implemented by the conventional droop method [9]. Further, a secondary control is implemented in a central master that calculates the corrective term to eliminate the steady-state voltage and frequency deviation, and then it is sent to the slaves [13]. These approaches require high bandwidth communication among the sources. The main drawback of this approach is that it represents a single point of failure and then replicas are required to improve reliability. Finally, in distributed approaches, each source uses a sparse communication network for data exchange among inverters. The units use only the neighborhood data and calculate its controller using both local measures and data from neighborhood units [14]–[25]. The main advantage of distributed control is the increasing system reliability, decreasing its sensitivity to failure.

Furthermore, many of these control methods are usually synthesized using small-signal equations or suffer from incomplete plant dynamics by using static equations for the active and reactive powers [26]-[28]. Besides, they assume purely inductive or resistive line impedances. Nevertheless, the microgrid impedance may change across time when the sources, loads, and storage devices are connected or disconnected. The mismatch of the line impedance results in inaccurate power sharing and decrease stability [29]. To address the above-mentioned drawback, the droop method may be implemented with a virtual impedance in lines with a high resistance/inductance (R/X) ratio [30]. However, selecting a large value of virtual impedance, the microgrid sensitivity to the feeding loads drastically increases. Moreover, the distortion in the output voltages also increase with unbalanced or nonlinear loads using a virtual impedance.

Recently, distributed controllers attracted more attention in microgrid applications as a promising approach [21]–[25]. In [21], a consensus-based frequency control is proposed for frequency restoration. Further, a distributed finite-time control approach is used in the voltage restoration. Moreover, in [22], a distributed terminal sliding mode controller for the state regulators is applied to achieve active power sharing and state of charge matching. In [23], a distributed SMC is presented

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for both, frequency and voltage restoration along with accurate active power sharing in islanded microgrid. Besides, in [24], an extended-state-observer secondary voltage and frequency

control using an adaptive super twisting algorithm is proposed. This approach is robust against models uncertainties and measurement noise. However, these controllers do not take into account the reactive power sharing. Furthermore, in [25], a distributed primery/secondary droop-inspired control is presented. In this approach, a distributed voltage variance

observer is proposed which uses two separate regulator to achieve active and reactive power sharing, respectively. In spite of achieving power sharing and restoring the frequency/voltage deviation, these controllers assume purely inductive lines and

are built on top of the artificially imposed separation of concerns that control each type of power, active and reactive, separately. Moreover, in [31] and [32], it has been shown that the system may become unstable in the presence of communication partition for different distributed control approaches.

In this paper, a novel primery/secondary distributed SMC for an islanded AC microgrid is proposed. Unlike the control methods presented above, the model-based control presented in this paper is obtained from a dynamic model based on active and reactive power dynamics equations. The proposed solution is theoretically analyzed for a wide range of line impedances. Moreover, the use of the SMC technique improves the dynamics performance, providing a fast transient response, and robustness against external disturbance and system parameters uncertainties [33]. Finally, the features of the proposed control are the following:

- 1) High resilience to line impedance values, since the model-based control takes into account that the active and reactive power are coupled.
- High robustness against system parameters deviations and external perturbations.
 Sinusoidal three-phase voltages are obtained even in case
- Sinusoidal three-phase voltages are obtained even in case of feeding unbalanced loads.
- 4) It achieves accurate active and reactive power sharing.
- 5) The secondary control is eliminated since frequency and voltage restoration are not necessary.
- 6) Only a low bandwidth communication network is necessary, thus reducing the traffic of control data.
- Robustness against communication failures, (e.g. communication partition). This is in direct contrast with the performance of other distributed control approaches that become unstable in case of communication failures [31], [32].

This paper is organized as follows. In section II, the largesignal model of the microgrid is presented. Section III shows the proposed distributed SMC. A stability analysis is presented in section IV. Experimental results are shown in section V. Finally, section VI draws some conclusions of this proposal.

II. MODELING OF A VOLTAGE SOURCE INVERTER CONNECTED TO AN AC MICROGRID

Fig. 1 illustrates the equivalent circuit of an inverter-based AC microgrid. Focusing on the local behavior of the i^{th} three-phase voltage source inverter (VSI), the VSI_i output voltage

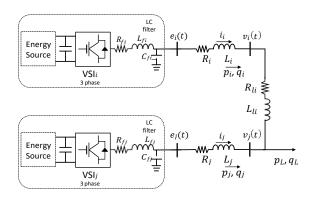


Fig. 1. Schematic diagram of an inverter-based AC microgrid.

can be defined as $e_i(t) = E_i \angle w_o t + \varphi_i$ and the voltage at the connection bus is defined by $v_i(t) = V_i \angle w_o t$. In this figure, E_i and V_i are the amplitude of the VSI_i output voltage and bus voltage, respectively, w_o is the microgrid frequency, φ_i is the phase angle of the VSI_i output voltage, R_i and L_i are the output impedance components, i_i is the VSI_i output current, and p_i and q_i are the instantaneous active and reactive power delivered by the VSI_i, respectively.

In the $\alpha\beta$ frame, the equations of the *i*th instantaneous active and reactive powers can be expressed as follows:

$$\boldsymbol{p}_{i} = \frac{3}{2} \left(\boldsymbol{v}_{i,\alpha} \boldsymbol{i}_{i,\alpha} + \boldsymbol{v}_{i,\beta} \boldsymbol{i}_{i,\beta} \right) \tag{1}$$

$$q_i = \frac{3}{2} \left(v_{i,\beta} i_{i,\alpha} - v_{i,\alpha} i_{i,\beta} \right)$$
(2)

Then, from Fig.1, the differential equations for the i^{th} output current i_i are:

$$\frac{di_{i,\alpha}}{dt} = \frac{1}{I_{i}} \left(e_{i,\alpha} - i_{i,\alpha} R_{i} - v_{i,\alpha} \right)$$
(3)

$$\frac{di_{i,\beta}}{dt} = \frac{1}{L_i} \left(e_{i,\beta} - i_{i,\beta} R_i - v_{i,\beta} \right)$$
(4)

It should be noted from Fig. 1 that, the VSIs dynamics are coupled with each other due to the interconnecting line impedances, R_{li} and L_{li} . These coupling effects are implicit in the bus voltage $v_i(t)$ and can be assumed to be known and bounded below the allowed tolerance [34]. These quantities can be obtained by measurements or estimated from (3) and (4).

The dynamic equations of the instantaneous active and reactive powers can be obtained by taking the first time derivative of (1)-(2), yielding:

$$\frac{dp_{i}}{dt} = \frac{3}{2} \quad v_{i,\alpha} \frac{di_{i,\alpha}}{dt} + i_{i,\alpha} \frac{dv_{i,\alpha}}{dt} + v_{i,\beta} \frac{di_{i,\beta}}{dt} + i_{i,\beta} \frac{dv_{i,\beta}}{dt}$$
(5)
$$\frac{dq_{i}}{dt} = \frac{3}{2} \quad v_{i,\beta} \frac{di_{i,\alpha}}{dt} + i_{i,\alpha} \frac{dv_{i,\beta}}{dt} - v_{i,\alpha} \frac{di_{i,\beta}}{dt} - i_{i,\beta} \frac{dv_{i,\alpha}}{dt}$$
(6)

Then, using the definition of $e_i(t)$ and $v_{b,i}(t)$ and (3) - (4) in (5) and (6), the dynamic equations of the active and reactive powers delivered by the VSI_i to the microgrid can be rewritten

$$\frac{dp_i}{dr} = \frac{3}{r} - V^2 + E V \cos(\varphi) - \frac{\kappa_i}{r} p + D \qquad (7)$$

$$\frac{da_i}{da_i} = \sum_{i=1}^{N} \sum_{j=1}^{N} \sum_{i=1}^{N} \sum_{i=1}^{N}$$

$$dt = -\frac{1}{2L_i}E_iV_isin(\varphi_i) - \frac{1}{L_i}q_i + D_q$$
(8)

where D_p and D_q are the inverters coupled dynamic terms represented as known disturbances in this model. These disturbances include the unbalanced voltages and nonlinear loads presented in the microgrid and are expressed as:

$$D_{p}(t) = \frac{3}{2} \quad i_{i,\alpha} \frac{dv_{i,\alpha}}{dt} + i_{i,\beta} \frac{dv_{i,\beta}}{dt} \tag{9}$$

$$D_{q}(t) = \frac{3}{2} i_{i,\alpha} \frac{dv_{i,\beta}}{dt} - i_{i,\beta} \frac{dv_{i,\alpha}}{dt}$$
(10)

Besides, taking the first derivative of p_i and q_i equal to zero, the steady-state expressions of the active and reactive powers can be written as:

$$P = \frac{S V_i}{2 Z_i} [(Ecos(\varphi_i) - V_i)cos(\varphi_i) + E sin(\varphi_i)sin(\varphi_i)]$$

$$i = \frac{3 V_i}{2 Z_i} [(Ecos(\varphi_i) - V_i)sin(\varphi_i) - E_i cos(\varphi_i)sin(\varphi_i)]$$
(12)

Note that, in previous works, the control architecture is based on steady-state expressions P_i and Q_i [9]–[16].

In this paper, unlike existing works, the controller is derived using the dynamic equations (7) and (8). However, it is considered that the phase angle φ_i is relatively small in practice, then the approximations $\cos(\varphi_i) \approx 1$ and $\sin(\varphi_i) \approx \varphi_i$ can be used. Finally, taking into account the above assumptions,

(7) and (8) can be rewritten as:

$$\frac{dp_i}{dt} = \frac{3}{2L_i} - V_i^2 + E V_i - \frac{R_i}{L_i} p_i + D_p \qquad (13)$$

$$\frac{dq_i}{dt} = -\frac{3}{2L_i} \underbrace{E}_{i} \bigvee_{i} \varphi_i - \underbrace{R}_{i} q_i + D_{i} \qquad (14)$$

Note that, the presented microgrid model is a multi-input multi-output (MIMO) nonlinear system.

III. PROPOSED CONTROL SYSTEM

A. Control objectives

The main objective of a VSI connected to an islanded microgrid is to regulate the frequency and amplitude of the microgrid voltage. Furthermore, with the aim to avoid the overstressing and deteriorating of the source, it is required to share the total load demand among the sources (i.e. active and reactive power sharing). Taking this in mind, the objectives of the proposed controller are the following:

- The voltage amplitude is a local variable in a microgrid with different values for each inverter in steady-state. Thus, the first objective is to regulate the voltage amplitude so that the average amplitude considering the other inverters coincides with the nominal value.
- 2) The frequency is a global variable in a microgrid with the same value for each inverter in steady state. Thus,

the second objective is that the steady-state frequency of each inverter matches the nominal frequency.

- 3) The third objective is to guarantee active and reactive power sharing
- In order to achieve the above objectives, in this paper a

novel distributed SMC for VSIs connected to a microgrid is proposed. Furthermore, to achieve accurate power sharing and to regulate the voltage amplitude, the following error functions are proposed:

$$e_{p,i} = p_i^* - p_i \tag{15}$$

$$e_{q,i} = q_i^* - q_i \tag{16}$$

$$\boldsymbol{e}_{\boldsymbol{E},i} = \boldsymbol{E}_o^* - \boldsymbol{E}_{a,i} \tag{17}$$

where p_i^* and q_i^* are the active and reactive power references, respectively, E_o^* is the voltage amplitude reference and $E_{o,i}$ is the average microgrid voltage. To calculate the power references and the average microgrid voltage, the proposal uses communication between the n_i neighborhood VSIs. Thus, the references are calculated as follows:

$$p_{i}^{*} = p_{i}^{max} \sum_{i=1}^{n_{i}} \frac{a_{ij}p_{j}}{n_{i}}$$
(18)

$$q_{i}^{*} = q_{i}^{max} \sum_{i=1}^{j_{n_{i}}^{n_{i}}} \frac{a_{ij}q_{j}}{n_{i}}$$
(19)

$$E_{a,i} = \frac{\sum_{i=1}^{n} \underline{\alpha_{ii}} \underline{E_i}}{n_i}$$
(20)

where p_i^{max} and q_i^{max} are the maximum active and reactive power supplied by source *i*, and p_j and q_j are expressed as:

$$p_{j}^{rated} = \frac{p_{i}}{p_{j}^{max}}$$
(21)

$$q_j = \frac{1}{q_j^{max}}$$
(22)

Finally, the communication system model is a connected graph with a spanning tree, where the coefficients a_{ij} determine the availability of communication between inverters *i* and *j*, thus indicating the set of nodes n_i that exchange control data. Hence, $a_{ij} = a_{ji} = 1$ if nodes *i* and *j* can exchange their information and $a_{ij} = a_{ji} = 0$ otherwise.

B. Proposed sliding mode control

In order to reach the control objectives presented above, the voltage amplitude and phase angle are expressed as:

$$\varphi_i = u_{\varphi_i} \tag{23}$$

$$E_i = V_o + u_{E_i} \tag{24}$$

where V_o is microgrid nominal voltage. Moreover, in order to avoid the chattering problems, and allow continuous control actions, a high-order sliding mode controller is proposed [35], [36]. Therefore, the first time derivative of u_{φ_i} and u_{E_i} are expressed as follows:

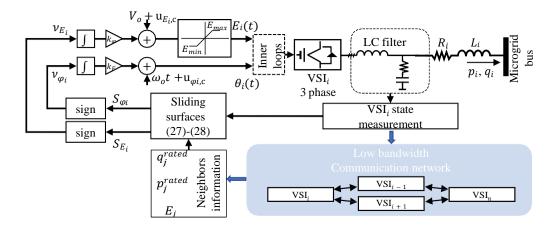


Fig. 2. Diagram of the proposed control scheme

$$\frac{du_{\varphi_i}}{dt} = k_{\varphi} v_{\varphi_i} + v_{\varphi_i, eq}$$
(25)

$$\frac{du_{E_i}}{dt} = k_E v_{E_i} + v_{E_i} e_q \tag{26}$$

where v_{E_i} and v_{φ_i} are the switching control variables, and $v_{E_i,eq}$ and $v_{\varphi_i,eq}$ are the equivalent controls.

Then, the proposed sliding surfaces are expressed as:

$$S_{E,i} = -\frac{dp_i}{dt} + k e_{p p,i} + k e_{\delta E e,i}$$
(27)

$$S_{\varphi,i} = \frac{\underline{dq}_i}{dt} - k_q e_{q,i} \tag{28}$$

where k_p , k_q and $k_{\delta E}$ are positive definite. In the sliding mode regime, the sliding surfaces $S_{\varphi,i} = 0$ represent the precise tracking of reactive power with a first order dynamic behavior. On the other hand, when $S_{E,i} = 0$, the voltage amplitude and the active power dynamic in sliding mode regime is quite similar to a resistive droop controller with a proportional secondary control [4]. Moreover, in steadystate the controller is allowed to regulate the average voltage amplitude and the active power sharing.

C. Control law

In the sliding mode controller, the control law usually consists of the equivalent control and the switching control. The equivalent control keeps the state of the system on the sliding surface, while the switching control forces the system sliding on the sliding surface.

First, in order to satisfy reaching conditions of sliding mode controllers $S_{E,i}S_{E,i} < 0$ and $S_{\varphi,i}S_{\varphi,i} < 0$, we must choose the switching controls whose control laws are expressed as follows:

$$v_{E,i} = \begin{array}{c} 1 & if \quad S_{E,i} > 0 \\ -1 & if \quad S_{E,i} < 0 \end{array}$$
(29)

$$v_{\varphi,i} = \begin{array}{cc} 1 & if \quad S_{\varphi,i} > 0\\ -1 & if \quad S_{\varphi,i} < 0 \end{array}$$
(30)

Second, assuming that the system is in the sliding mode regime, and taking into account that in sliding regime it is accomplished that S = S = 0. The integral of the equivalent controls $v_{E_{\nu}eq}(t)$ and $v_{\varphi_{\nu}eq}(t)$ can be obtained by taking the sliding surfaces equal to zero. Thus, considering the microgrid model (13) and (14), the voltage amplitude and angle definitions (25) and (26), and the sliding surfaces (27) and (28), the following expressions can be obtained:

$$\int_{0}^{t} t v_{\varphi_{i},eq} dt \, \overline{3} V_{i} E_{i} - \frac{R_{i}}{L_{i}} q_{i} + D_{q} - \frac{2L_{i}}{3V E} (k_{q} e_{q,i})$$

$$\int_{0}^{t} V_{E_{i},eq}dt = (V_{i} - V_{o}) + \frac{2L_{i}}{3V_{i}} \frac{R_{i}}{L_{i}}p_{i} - D_{p} + \frac{2L_{i}}{3V_{i}} (k e + k e)$$
(31)
(31)
(32)

where R_i and L_i are the local inverter impedances (see Fig. 1). Furthermore, it is worth noting that the disturbances modeling in (9) and (10) are used as feedforward terms, thus bringing robustness against the coupled inverters dynamics and microgrid perturbation, such as unbalanced and nonlinear loads.

Finally, according to the control laws (29) and (30), and (31) and (32), the voltage amplitude and phase angle set points, (23) and (24), respectively, can be rewritten as follows:

$$\varphi_{i} = k_{\varphi} \int_{0}^{t} sign(S_{\varphi,i})dt + \int_{0}^{t} v_{\varphi_{i},eq}dt \qquad (33)$$

$$E_i = V_o + k_E \int_0^{\infty} sign(S_{E,i})dt + \int_0^{\infty} v_{E_i,eq}dt$$
(34)

where k_{φ} and k_{E} are the control gains.

It should be noted that the discontinuous sign functions only appear in the time derivatives of the actual control inputs, thus allowing continuous control actions. Besides, the control law does not depend on the line impedances, hence the virtual impedances are not needed.

D. Stability analysis

The stability analysis is carried out using the Lyapunov criteria. Let V_e and V_{φ} be the Lyapunov function candidates,

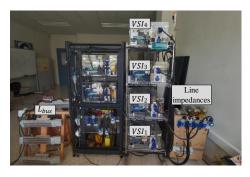


Fig. 3. Photograph of the experimental microgrid.

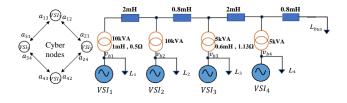


Fig. 4. Diagram of the laboratory microgrid setup.

such that

$$V_{e} = \frac{1}{2} \frac{S^{2}}{E,i}$$
(35)

$$V_{\varphi} = \frac{1}{2} \frac{S^2}{\varphi, i} \tag{36}$$

the time derivative of (35) and (36) can be expressed as follows:

$$\dot{V}_e = S_{E,i} \dot{S}_{E,i} \tag{37}$$

$$\dot{V_{\varphi}} = S_{\varphi,i} \dot{S_{\varphi,i}}$$
(38)

Then, in order to prove the stability, it is sufficient to achieve

the following conditions $\dot{V}_e < 0$ and $\dot{V}_{\varphi} < 0$. Therefore, the stability is achieved if the above conditions are satisfied. Taking this in mind, from (25), (26), (31), (32), (33) and (34), and using (27) and (28), the above equations can be rewritten as follows:

$$\dot{V}_e = S_{E,i}(-k_E sign(S_{E,i})) \tag{39}$$

$$V_{\varphi} = S_{\varphi,i}(-k_{\varphi}sign(S_{\varphi,i})) \tag{40}$$

From (39) and (40), it can be noted that the time derivative of Lyapunov function is definitely negative, hence the control system becomes asymptotically stable. Additionally, it should be noted that even under the absence of communication network the stability condition is still achieved.

E. Controller implementation

In this section the proposed controller implementation is presented.

Fig. 2 shows the proposed control scheme for the i^{th} VSI. The proposal is based on two sliding surfaces to generate the output voltage references of each VSI. As can be seen

TABLE I OUTPUT IMPEDANCES

Description	Symbol	Value		
VSI 1 output impedance	<i>Z</i> 1	0.5 + 2.64j Ω		
VSI 2 output impedance	Z 2	0.5 + 2.64 <i>j</i> Ω		
VSI 3 output impedance	Z ₃	0.5 + 1.88j Ω		
VSI 4 output impedance Z_4 0.5 + 1.88 j Ω				

TABLE II EQUIVALENT LINE IMPEDANCE

Description	Value	(X/R) ratio
Impedances seen by VSI 1	0.76 + 1.34 <i>j</i> Ω	1.76
Impedances seen by VSI 2	0.80 + 0.74 <i>j</i> Ω	0.92
Impedances seen by VSI 3	1.32 + 0.87 <i>j</i> Ω	0.30
Impedances seen by VSI 4	$1.30 + 0.76 j \ \Omega$	0.59

in (27), (28), (31) and (32), the control laws (33) and (34) use local and neighborhood data. In order to achieve the control objectives, the communication system model is a connected graph with a spanning tree, where the neighborhood inverters exchange their voltage amplitude, active and reactive rated power, and thus the errors are calculated. For this purpose, the control system uses a low bandwidth communication network to interchange data between the neighborhood sources. Additionally, when the communications fail, the proposed controller could work with the local data remaining the system stability, such as the droop control, however, the steady-state could be compromised [37].

Then, once the local and neighborhood data is obtained, the sliding surfaces and the control variables $u_{\varphi_{i,c}d}$, $u_{E_{i,c}d}$, $u_{\varphi_{i,c}}$ and $u_{E_{i,c}}$ are calculated to obtain the voltage amplitude and angle phase set points using (23) and (24). Moreover, in order to maintain the allowed tolerance [34], E_i is bounded between ±5% of the nominal value, as it can be seen in Fig.

2. Finally, once the voltage set point is computed, the inner loop is responsible to generate the voltage signals. Therefore, voltage set point used by the inner loop is expressed as follows:

$$e_i^* = E_i \sin(\vartheta_i) \tag{41}$$

$$\vartheta_i = \omega_o t + \varphi_i. \tag{42}$$

IV. EXPERIMENTAL RESULTS

A. Microgrid Setup

This section presents the experimental tests implemented in the laboratory microgrid shown in Fig. 3 and 4. Fig. 4 illustrates the diagram of the laboratory inverter-based islanded microgrid considered in this study. This is formed by four inverters working as VSIs, each one with local loads that may be connected or disconnected. Moreover, two threephase balanced loads are considered to be connected to the microgrid each that may be connected or disconnected in the position shown in Fig. 4 to form the global load L_{bus} .

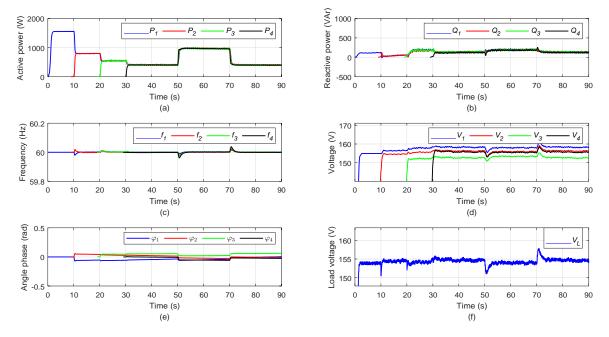


Fig. 5. Performance evaluation of the proposed controller: (a) inverter active power, (b) inverter reactive power, (c) inverter frequencies, (d) bus voltage, (e) phase angles and (f) load voltage.

Moreover, from Table I the output impedance of each inverter is presented. In Table II, it should be noted that the inverter 1 line impedance is mainly inductive while the inverter 3, and 4 line impedances are mainly resistive. In addition, the inverter 2 line impedance presents no dominant behavior. Taking all these impedances in mind, it is clear that the microgrid is operating in a mixed scenario with resistive, inductive and complex impedances. Each inverter was built using a 2.3-kVA Guasch MTL-CBI0060F12IXHF full bridge as the power converter and is driven by a 32-bit dual-core DSP, the Concerto-F28M36P63C with a sampling frequency of 10 kHz. This device is composed of a C28 DSP core for control purposes and a Cortex M3 ARM for communication. The communication between each VSI is based on the UDP Protocol over an Ethernet link that communicates the four C28 cores through the M3 cores with a transmission time of 0.1 s. The communication configuration is represented by the cyber nodes shown in Fig. 4. The bidirectional arrows represent the availability of communication between inverters. Finally, the control gains k_{φ} and k_{E} have been selected as $k_{\varphi} = 0.1$ and $k_E = 1$ as can be seen in table III.

B. Performance evaluation in Normal Operation

To validate the performance of the proposed distributed SMC with the parameters shows in Table III, the following experimental test was designed in the laboratory setup. First, to validate the plug and play capability, the four VSIs are connected at different time, t = 0, 10, 20 and 30 s respectively. The first inverter starts and fixes the microgrid frequency and voltage, to their rated values, while starting to feed a balanced load connected in the bus, with a total power demand above 1.6 kW. The activation of the following inverters is done using a phase-locked loop for synchronizing them to the

TABLE III SYSTEM PARAMETERS

Description	Symbol	Value
Microgrid voltage	Vo	110 Vrms
Microgrid frequency	f	60 Hz
Nominal dc-link voltage	Vdc	400 V
Active power gain	k _p	250
Reactive power gain	k _q	250
Voltage amplitude gain	k _{δE}	1000
Voltage control gain	k_E	1
Phase angle control gains	k_{arphi}	0.1
Maximum active power 1-4	$P_{1,2,3,4}^{max}$	2000 W
Maximum reactive power 1-4	$Q_{1.2.3.4}^{max}$	2000 VAr
Sampling frequency	f_s	10 kHz

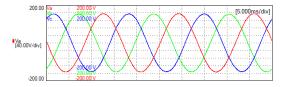


Fig. 6. Load voltages using the proposed control scheme.

microgrid voltage phase. Then, to validate the performance of the microgrid under sudden load change, the second three-phase balanced load is connected and disconnected at t = 50 and 70 s respectively.

Fig. 5 shows the active and reactive power by each inverter, as well as their frequencies, voltage amplitudes, the phase angles φ_i and the amplitude correction terms δE_i . Fig. 5 (a) and Fig. 5 (b) show active and reactive powers, as it can

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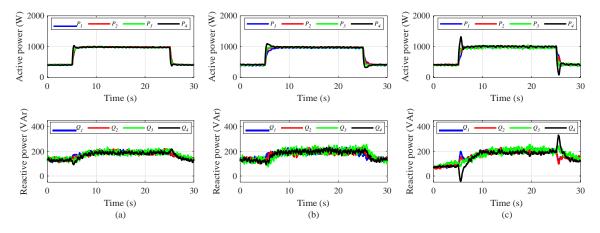


Fig. 7. Active and reactive power transient responses with different transmission rates: (a) 0.1 s, (b) 0.5 s and (c) 1 s.

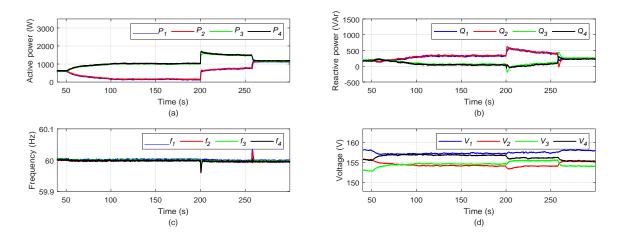


Fig. 8. System dynamics when a communication failure leads to two partitions: (a) inverter active power, (b) inverter reactive power, (c) inverter frequencies, and (d) bus voltage.

be seen after each connection the active and reactive power sharing is achieved with a good dynamic response. Moreover, Fig. 5 (c) shows that all inverter frequencies are synchronized to the rated frequency of 60 Hz in steady state. Further, Fig. 5 (e) shows that the phase angles are small and smooth. Moreover, Fig. 5 (d) show two interesting points. First, the amplitude of the output voltages are different in each inverter. And second, the average value of these voltage amplitudes in steady-state is perfectly regulated to the nominal value V_0 . Finally, Fig. 5 (f) shows the amplitude of the load voltage, and it can be seen that the voltage is regulated with a tolerance less than **5**% of its nominal value. These characteristics are maintained during the whole experiment with different loads and number of inverters in operation. Thus, it is demonstrated that the proposed controller achieves the control objectives presented in section III. Additionally, Fig. 6 shows the voltage waveforms of the load bus. It can be noted that the voltage waveforms are balanced with sinusoidal form.

C. Communication Failure Study

The distributed control system relies on the availability of communications. Besides, in large distributed systems the units may be far away, thus the communication services may be affected, such as transmission delay, communication link failures, etc. Accordingly, communication failures may compromise the overall control performance and the system may be driven to instability. Moreover, recent studies have shown that for different distributed control approaches the microgrid may become unstable when a communication partition is presented [31], [32]. Thus, to show the performance of the proposed control in the presence of communication failures, two different tests were designed.

In the first experiment, the proposed control has been tested under a sudden load change with different transmission rates. Fig. 7 shows the active power and reactive power of the four VSIs under different transmission rates. Fig. 7(a) shows the active and reactive power performance with the nominal transmission rate of 0.1. Besides, in Fig. 7(b) and (c) the active and reactive power transient responses are shown with a transmission rate of 0.5 and 1 s, respectively. From Fig. 7 it should be noted that the steady-state is not compromised by the transmission delay. However, the transient response of both active and reactive power is slightly deteriorated when the transmission rate increases.

In the second experiment, the proposed control has been tested in presence of communication links failure. For this purpose the following experiment was designed. At the time t = 0

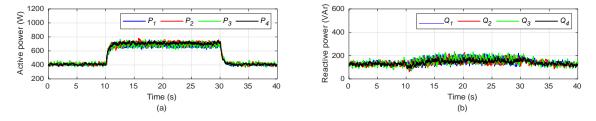


Fig. 9. System dynamics feeding an unbalanced load: (a) inverter active power and (b) inverter reactive power.

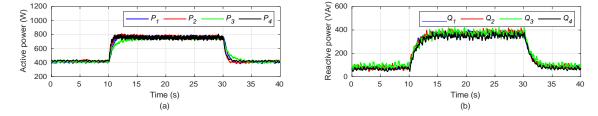


Fig. 10. System dynamics feeding nonlinear load: (a) inverter active power and (b) inverter reactive power.

s, the four VSIs are previously working in normal condition feeding a three-phase balanced load. Then, at time t = 50 s the communication links 1-3 and 2-4 have been disabled during 250 s, and at time t = 300 s, communications are reestablished. Besides, the second three-phase balanced load is connected at t = 200 s. Hence, the system emulates a partition in the communication network. Fig. 8 shows the active and reactive power, as well as the frequencies and voltage amplitude of each VSI when the communication partition occurs. It should be noted that after the partition occurs, all inverter frequencies are synchronized to the rated frequency of 60 Hz and the average voltage amplitude is regulated to the nominal value V_o . However, the active and reactive power sharing between the two communication islands is not achieved. It is because the distributed control starts working as "two" parallel controls independent among them. Despite this, the proposed control avoids the instability problem. This is a superior feature of the proposed control in comparison with others [31], [32]

D. Three-Phase Unbalanced Load Study

In the third experiment, the proposed controller has been tested under a three-phase unbalanced load. For this purpose, the following experiment has been designed. The four VSIs start feeding a three-phase balanced load, then an unbalanced load is connected and disconnected in L_{bus} at time t = 20 and 40 s, respectively. Fig. 9 shows the active and reactive power of each VSI. Moreover, Fig. 11 shows the current and voltage waveforms of inverter 1. In Fig. 9, it can be seen that after the unbalanced load is connected, the active and reactive power sharing is achieved. As a promising characteristic, note that the proposed control does not need a virtual impendence due to its robustness against line impedance uncertainties, as it was demonstrated in section IV. Accordingly, the VSIs voltage waveforms are balanced even with unbalanced current waveforms, as it has been shown in Fig. 11.

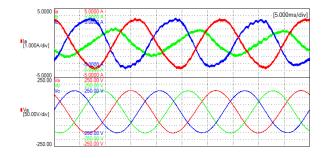


Fig. 11. Inverter 1 current and voltage using the proposed control scheme under unbalanced load.

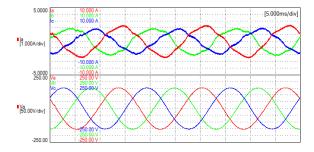


Fig. 12. Inverter 1 current and voltage using the proposed control scheme feeding a nonlinear load.

E. Nonlinear Load Study

Finally, the proposed controller has been tested under a nonlinear load (three phase diode rectifier). Taking this in mind, the following experiment has been designed. The four VSIs start feeding a three-phase balanced load, then a nonlinear load is connected and disconnected in L_{bus} at time t = 20 and 40 s, respectively. Fig. 10 shows the active and reactive power. Besides, Fig. 12 shows the current and voltage waveforms of inverter 1, respectively. From Fig. 10, it should be noted that the active and reactive power sharing is achieved even feeding a nonlinear load. Finally, in Fig 12 it should be noted that the current waveforms are distorted with a THD of 15.1%.

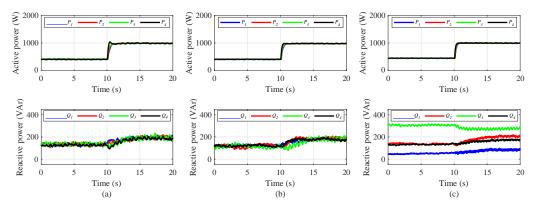


Fig. 13. Active and reactive power transient response comparison of different control schemes: (a) proposed controller, (b) Droop-free control [25] and (c) Distributed robust secondary control [24].

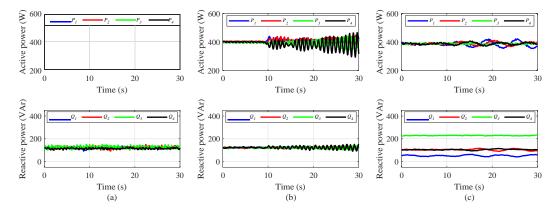


Fig. 14. Active and reactive power steady state behavior without virtual impedance: (a) proposed controller, (b) Droop-free control [25] and (c) Distributed robust secondary control [24].

Meanwhile, the voltage waveforms have a sinusoidal form when the nonlinear load is connected with a THD of 1.5% approximately.

F. Comparison with State-of-the-art Techniques

This subsection presents a comparison between the proposed control and previous state-of-the-art controllers. For this purpose,the proposal will be compared with the droopfree controller [25] and a distributed robust secondary control [24]. It is worth noting that the control parameters of each policy have been defined to obtain similar dynamics among them. Moreover, to yield stability and increase the accurate power sharing, the droop-free and the distributed robust secondary control need a virtual impedance [30].

In order to validate the dynamic performance of the proposed control scheme, two experiments have been performed. First, the controllers have been tested under a sudden load change. This experimental test has the following pattern. From t = 0 s to t = 10 s the inverters start feeding a balanced load connected in L_{bus} , with a total power demand above 1.6 kW. Then, a second balanced load is connected in L_{bus} at time t = 10. Fig. 13 shows the experimental results of the considered control schemes under a sudden load change. It should be noted that the active power transient response is similar in the three control schemes. However, from Fig. 13

(c) it can be seen that the reactive power is not shared using the method in [24].

In the second experiment, the controllers [25] and [24] are initially driven with a virtual impedance. Then, at t = 10 s the virtual impedance is removed from the control schemes [25] and [24]. It is worth noting that the proposed controller does not need virtual impedance. Fig. 14 shows the active and reactive power by each inverter using the proposed SMC, [25] and [24]. It could be noted that, when the virtual impedance is removed, both the droop-free controller [25] and the distributed robust secondary control [24] dynamics is oscillatory and unstable. It is due to the power lines mismatches [29], since the microgrid is operating in a mixed scenario with resistive, inductive and complex impedances, as it can be seen in Table II.

Finally, Table IV is presented to compare the proposed controller with the state-of-the-art controllers. The comparison is done in terms of accurate active and reactive power sharing, steady-state voltage and frequency deviation, resilience to line impedance ratio and robustness against communication partitions. According to the experimental results and Table IV, it can be seen that the proposed control is more beneficial in terms of accurate active and reactive power without voltage deviation and robustness against communication failures.

TABLE IV COMPARATIVE ANALYSIS

Control Method	Power sharing	f and V regulation	Resilience to R/X ratio	Comms. partition
[21]	only p	Yes	No	instable
[22]	only p	Yes	No	Not shown
[23]	only p	Yes	No	Not shown
[24]	only p	Yes	No	Not shown
[25]	<i>p</i> and <i>q</i>	Yes	No	instable
Proposed	p and q	Yes	Yes	stable

V. CONCLUSIONS

A novel distributed direct power sliding mode control has been proposed in this paper for AC microgrids. This control relies on the availability of communications. The control is formed by two sliding surfaces based on the microgrid model equations, with the following properties. On the one hand, the controller regulates the average voltage across the microgrid to the nominal value. On the other hand, the controller compares the local active and reactive power with the neighbors, to achieve a zero steady-state error in active and reactive power sharing. Besides, a stability analysis is presented to probe the controller robustness against line impedance uncertainties. Experimental results show that the proposed controller provides voltage regulation and a precise active and reactive power sharing, as well as frequency synchronization to the rated value under various load types, even in case of unbalanced loads. Moreover, the experimental results have also shown that the proposed control presents both plug and play capability and high robustness against communication failures.

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