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# Analysis of a Direct Microcontroller Interface for Capacitively-Coupled Resistive Sensors

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Abstract— A novel approach to directly interface a capacitivelycoupled resistive sensor to a microcontroller is presented in this paper. The existing measurement schemes for such sensors are complex. In addition, the coupling capacitance often also holds important data. The proposed simple measurement system, for such series RC sensors, is capable of measuring both the resistance and the coupling capacitance. A detailed analysis on the effect of the non-idealities on the resistance measurement showed that it is independent of the accuracy of the charging capacitor, supply voltage and preset threshold voltage. The performance of the proposed scheme has been evaluated by building suitable prototypes. Initially, a setup was designed such that the measurement was not limited by the non-idealities of the microcontroller. The test results from this showed a maximum error of 0.28% and 0.96% for the resistance and capacitance measurement, respectively. The subsequent study with the microcontroller interface exhibited a maximum error of 0.91% (resistance) and 2.94% (capacitance). Noise and resolution studies have also been conducted and the results presented. The accuracy of the prototype is promising, with a measurement time of 5 ms per parameter. This is a practical, low-power, low-cost measurement system as it provides digital data on the resistance and capacitance, in series, using only a microcontroller, and a couple of passive components.

*Index Terms*—Interfacing circuit, resistive sensor, capacitive sensor, series RC, non-contact, capacitive-coupling, direct microcontroller interface, measurement system.

#### I. INTRODUCTION

APACITIVELY-coupled resistive sensors offer an efficient way to perform non-contact measurements. In these sensors, the measurement system accesses the sensing resistance through the coupling capacitances that form as a part of the sensor structure. These capacitances enable non-intrusive measurement of the quantity of interest that modulates the value of the sensing resistance. To illustrate this further, let us take the example of non-contact measurement of water conductivity, which is one of the important parameters in assessing water quality. In this, the conductivity of a water column in an insulating tube (e.g. PVC pipe) is determined without exposing the measurement electrodes to the water [1], thereby avoiding the possibility of errors in the measurement due to corrosion and formation of layers of contamination on them [2]. As illustrated in Fig. 1(a), the electrode, the insulating pipe, and the water layer directly below the electrode and in contact with the insulation form a coupling capacitance. The resistance of the water column of interest can be measured, through this coupling

#### capacitance [1].

Resistive sensors are a popular choice to form touchpads [3], and tactile sensors [4]. Similarly, they are used to measure pressure [5], force [6], [7] and temperature [8]. Conventional resistive sensing methods require direct contact of the resistive element to the measurement circuit, thus limiting the reliability of the sensor and its versatility in the range of applications. Once a simple measurement scheme is available to read capacitively-coupled resistive sensors, some of the resistive sensors [3], [4] could be converted into capacitively-coupled ones. This will improve their reliability and the manufacturing process. Additionally, the final assembly could be simplified as the wired connection is no longer a requirement. Hence, another application, apart from water conductivity possible measurement, is in the area of resistive touchpads, wherein the change in resistance of the sensor can be measured through a capacitively-coupled arrangement. Although the focus of this paper is on measuring the resistance without direct contact between the measurement system and the resistive sensor, in certain applications, the value of the coupling capacitance also has useful information [9]. This is also addressed herein.

The schemes employed so far in the existing capacitivelycoupled resistive sensing applications [1], [10], [11] have their limitations. For instance, in [1] and [11], the circuits are relatively complex with the requirement of a precise sinusoidal excitation and a phase-sensitive detector. The need for several ICs and components for the analog and/or digital processing of the sensor signal leads to an increased power consumption. These circuit designs become complicated, mainly because the objective is to measure resistances in the range of tens of ohms  $(<100 \Omega)$ . Such levels of complexity cannot be justified when, for example, one aims to scale the circuit for an array of sensors, or measure higher resistance values. In fact, in the case of the conductivity measurement of water, the resistance of sea water  $(55000 \,\mu\text{S/cm} [12])$  in an insulated cylindrical column of length 10 cm and diameter 2.5 cm is in the range of a few hundred ohms, but that of a drinking water outlet (diameter 1/4 inch or 6.35 mm pipe could be sufficient) is a few hundreds of kohms. The conductivity of drinking (tap) water varies from 50-800 µS/cm [12]. For the capacitively-coupled measurement of higher resistances, a simpler scheme is preferred. This would be very promising in the water quality monitoring to determine the potability of drinking water [12] and the quality of river water [13], [14].

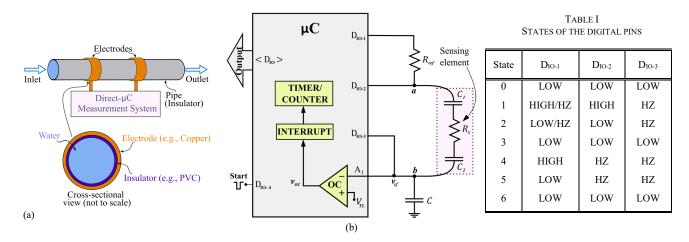


Fig. 1. (a) Illustration of non-contact conductivity measurement of water in an insulating pipe. (b) Block diagram of the proposed direct microcontroller interface for capacitively-coupled resistive sensor.

Direct microcontroller interfaces have been developed for the measurement of resistive [15]-[18], capacitive [19], [20] and inductive [21], [22] sensors. In such circuits, the microcontroller excites a passive RC or RL network and then measures the duration of the transient response to reach a predefined threshold, using the embedded digital timer. The main advantage is that no active electronic component is used between the sensor and the digital system, i.e. microcontroller, thus reducing the cost, size, and power consumption of the design. However, the possibility of such direct interfaces to measure capacitively-coupled resistive sensors has not been explored so far, except for the preliminary study in [23].

In this paper, a detailed study and evaluation of a simple and effective direct microcontroller interface suitable for the measurement of the resistance and coupling capacitance of a capacitively-coupled resistive sensor are presented. The estimated resistance is independent of the variation in the values of the coupling capacitances, the charging capacitor, the power supply voltage, and the preset threshold voltage. Similarly, the measured value of the coupling capacitance is not a function of the change in the sensor resistance. The proposed scheme is simple in design, consumes low power, and carries out the measurement in a few milliseconds. It does not use any expensive part and can hence realize a low-cost measurement system. The details of the proposed direct microcontroller scheme are presented below.

# II. DIRECT $\mu C$ Interface for a Capacitively-Coupled Resistive Sensor

A block diagram of the proposed direct microcontroller ( $\mu$ C) scheme is shown in Fig. 1(b), where  $R_x$  represents the resistive sensor element. In the example given in Fig. 1(a),  $R_x$  corresponds to the resistance of the water column between the electrodes. The coupling capacitance is formed between the electrode and the water column as it is separated by an insulator. The same is valid when an insulated electrode is kept inside the pipe [1].  $R_x$  is coupled to the measurement unit through the coupling capacitances  $C_1$  and  $C_2$  (refer Fig. 1(b)). As illustrated,  $R_x$ ,  $C_1$  and  $C_2$  are in series, and the nodes 'a' and 'b' are

accessible for the direct connection to the  $\mu$ C. The scheme uses a reference resistor  $R_{ref}$ , and a fixed capacitor C. As illustrated in Fig. 1(b), the measurement can be performed using four digital input/output pins D<sub>IO-1</sub>, D<sub>IO-2</sub>, D<sub>IO-3</sub>, and D<sub>IO-4</sub>, an analog input pin A<sub>1</sub>, a timer and a comparator (OC) embedded into the  $\mu$ C.

The two terminals of  $R_{ref}$  are connected to the two digital pins D<sub>IO-1</sub> and D<sub>IO-2</sub>. The terminal connected to D<sub>IO-2</sub> is also connected to node 'a'. This node is connected to  $C_1$ . One end of the  $C_2$  is accessible at node 'b'. The effective capacitance  $C_{12}$  due to the coupling capacitances  $C_1$  and  $C_2$  equals  $C_{12} =$  $(C_1C_2/(C_1 + C_2))$ . The fixed capacitance C is connected across node 'b' and ground. The node 'b' is also connected to D<sub>IO-3</sub> and A<sub>1</sub>, as given in Fig. 1(b). C, along with  $R_{ref}$ , and the capacitively-coupled resistive sensor, form the RC circuit that enables the measurement of  $R_x$  in this direct- $\mu$ C scheme. The operation of the scheme is explained below assuming ideal components. As for the microcontroller, the digital pins in HZ state are assumed to have an infinite input resistance, while those set as output, a zero output resistance. The non-idealities are considered and studied later in Section III.

# A. Measurement of $R_x$

To estimate  $R_x$ , in the proposed direct- $\mu$ C approach, the system operates in two modes modes based on if the sensing resistance  $R_x$  alone is present, or the reference resistance  $R_{ref}$ is also present. Thus, the modes representing these two scenarios are, mode -  $R_x$  and mode -  $(R_{ref} + R_x)$ , respectively. The measurement sequence starts with mode -  $R_x$ , followed by mode -  $(R_{ref} + R_x)$ . To execute this, the state of the pins D<sub>IO-1</sub>, D<sub>IO-2</sub>, and D<sub>IO-3</sub> are set as per Table-I, where LOW refers to 0 V or ground, HIGH to the dc voltage  $V_{DD}$ , and HZ indicates a high-impedance state. D<sub>IO-3</sub> is in HZ state during the charging and discharging cycles. A timing diagram of the measurement process is illustrated in Fig. 2. The measurement begins when a start pulse (high to low transition) is provided at pin D<sub>IO-4</sub>. Then, the  $\mu C$  sets  $D_{IO-1}$ ,  $D_{IO-2}$ , and  $D_{IO-3}$  to 0 V, to discharge any residual charges that may be present in the capacitances. This is indicated as state '0' in Fig. 2. Following this, D<sub>IO-2</sub> is set to HIGH, and D<sub>IO-1</sub> can be set to either HIGH or HZ. This is

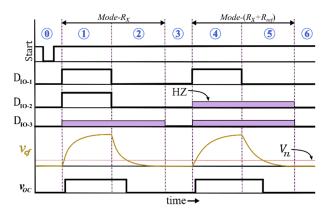


Fig. 2. Timing diagram of the proposed scheme. The signal 'start' indicates the output of  $D_{10-4}$ .

indicated as state '1' in Fig. 2. In Fig. 2 this charging-phase is called state '1'. In this phase of mode -  $R_x$ ,  $R_x$  along with  $C_1$ ,  $C_2$ , and C constitute the RC circuit. In this condition, C is charged to the final voltage  $V_F$  given by (1). The measurement unit is in state '1' for a preset time  $T_{Ch1}$ .  $T_{Ch1} >> (R_x C_{eq})$ , where  $C_{eq} = C_{12}C/(C_{12} + C)$ .

$$V_F = V_{DD} \frac{c_{12}}{c_{12} + c} \tag{1}$$

As soon as the time  $T_{Ch1}$  is over, the state '1' ends and the state '2' begins, as in Fig. 2. In state '2',  $D_{IO-2}$  is set to LOW, causing *C* to discharge through  $R_x$  as illustrated in Fig. 3(a), where  $I_{dch1}$  indicates the associated discharging current. In state '2',  $D_{IO-1}$  can be in either LOW or HZ states. In this discharging phase, the voltage  $v_{cf}$  across *C* is fed to the input of the comparator OC via  $A_1$ , and compared with a preset DC threshold voltage  $V_{TL}$ , as illustrated in Fig. 2. The time interval  $T_1$  taken for  $v_{cf}$  to reduce from  $V_F$  to  $V_{TL}$  is measured by the embedded timer, which starts counting when the discharge begins and stops when  $v_{cf} = V_{TL}$  (via interrupt to timer from OC output  $v_{OC}$ ). During the discharging phase in state '2', (2) can be written when  $v_{cf} = V_{TL}$ . It is assumed that  $V_F > V_{TL}$ , which can be ensured, in practice, by selecting a value of *C* such that  $C < C_{12}$ , and setting  $V_{TL} < V_{DD}/2$ .

$$V_{TL} = V_F e^{-T_1/R_x C_{eq}} \tag{2}$$

From (2), we can write:

$$T_1 = R_x C_{eq} \ln(\frac{V_F}{V_{TL}}) \tag{3}$$

The measurement unit is in state '2' for a preset time  $T_{Dh1}$ , where  $T_{Dh1} = T_{Ch1}$ . If  $R_x$  is computed using (3), its accuracy depends on the accuracy in the measurement of  $C_{eq}$ ,  $V_F$  and  $V_{TL}$ . To achieve an output independent of these parameters, another mode is introduced, as explained next. Once state '2' is completed, the  $\mu$ C sets  $D_{IO-1}$ ,  $D_{IO-2}$ , and  $D_{IO-3}$  once again to LOW (like in state '0'). This is indicated as state '3'.

In the mode -  $(R_{ref} + R_x)$ , the RC circuit has both  $R_x$  and  $R_{ref}$  in series. D<sub>IO-2</sub> is set to HZ throughout this mode. D<sub>IO-1</sub> is set to HIGH to charge *C* to  $V_F$ , and LOW for it to discharge through the same path. These are executed through the states

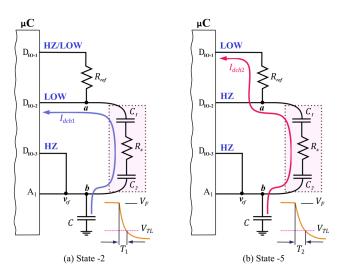


Fig. 3. Discharge path of C during (a) state '2', and (b) state '5'.

'4' and '5', respectively, as illustrated in Fig. 2. The preset intervals for the states '4' and '5' are  $T_{Ch2}$  and  $T_{Dh2}$ , where  $T_{Ch2} = T_{Dh2}$  and  $T_{Ch2}$ ,  $T_{Dh2} >> [(R_x + R_{ref})C_{eq}]$ . As in mode -  $R_x$ , the time interval taken by C to discharge from  $V_F$  to  $V_{TL}$  is measured. As illustrated in Fig. 3(b), the discharging current  $I_{dch2}$  flows through  $R_x$ ,  $R_{ref}$  and  $C_{12}$ . In this condition, the expressions for  $V_{TL}$  and the corresponding discharge time, namely  $T_2$ , are given by (4) and (5), respectively. The measurement of  $R_x$ . In case continuous measurements are to be made, the circuit operation enters state '6' once the discharge phase in mode -  $(R_{ref} + R_x)$  is completed. This state is identical to state '0' and facilitates the discharge of any residual charges.

$$V_{TL} = V_F e^{-T_2/(R_{ref} + R_x)C_{eq}}$$
(4)

$$T_2 = (R_{ref} + R_x)C_{eq}\ln(\frac{V_F}{V_{TL}})$$
(5)

Once  $T_1$  and  $T_2$  have been measured,  $R_x$  is estimated as follows. Dividing (5) by (3) gives (6).

$$\frac{R_2}{R_1} = 1 + \frac{R_{ref}}{R_x}$$
 (6)

By re-arranging (6), we get:

$$R_x = \frac{R_{ref}}{(T_2 - T_1)} T_1 \tag{7}$$

Thus, the resistance  $R_x$  of the capacitively-coupled resistive sensor can be determined in terms of the timer outputs  $T_1$  and  $T_2$ , and the known resistor  $R_{ref}$ . Since  $T_1$  and  $T_2$  are outputs from the digital timer, the final output is digital. As can be seen from (7), the output does not depend on the value of the other parameters of the system such as  $C_1$ ,  $C_2$ , C,  $V_F$  and  $V_{TL}$ , or any variations of those between two consecutive measurements of  $R_x$ . However, the measurement output does depend on  $R_{ref}$ . For this reason, such a reference component should be selected with a low tolerance and a low thermal sensitivity.

#### B. Measurement of $C_{12}$

In certain applications, the value of the effective coupling capacitance  $C_{12}$  carries useful information. For example, in the case of resistance measurement of the water column in pipes, the actual inner diameter of the pipe will change over long time, due to deposition of organic/inorganic particles and microorganisms. Thus, the value of  $R_x$  will be higher, for the same conductivity, compared to that measured with the original dimensions of the pipe. In such a scenario, the measurement of the coupling capacitance  $C_{12}$  can provide information about these inner layers formed and, hence, corrective measures can be taken. Hence, it would be beneficial if the proposed direct microcontroller interface could be extended to determine the value of  $C_{12}$ . Such a method can also be used to measure the capacitance value, in addition to the resistance value, in lossy capacitive sensors where the resistance and capacitance are in series, in the electrical equivalent circuit.

To measure  $C_{12}$ , the same two modes of operation, mode -  $R_x$  and mode -  $(R_{ref} + R_x)$  are used. The only difference is that in each mode an additional measurement corresponding to a different threshold  $V_{TLnew}$  is made, i.e. in each mode, two measurements are made, namely, one corresponding to  $V_{TL}$  and the other to  $V_{TLnew}$ , where  $V_{TLnew} < V_{TL}$ . This is illustrated in Fig. 4.

In mode -  $R_x$ , the two discharging times to be measured are:  $T_1$  for C to discharge to  $V_{TL}$ , and  $T_{1new}$  for C to discharge to  $V_{TLnew}$ . To determine  $T_{1new}$ ,  $V_{TL}$  and  $T_1$  in (2) are replaced with  $V_{TLnew}$  and  $T_{1new}$ , respectively:

$$V_{TLnew} = V_F e^{-T_{1new}/R_X C_{eq}} \tag{8}$$

Dividing (2) by (8), taking logarithm and re-arranging, we get:

$$T_{1new} - T_1 = R_x C_{eq} \ln\left(\frac{V_{TL}}{V_{TLnew}}\right) \tag{9}$$

Similarly, in mode -  $(R_{ref} + R_x)$ , in addition to measuring the discharging time  $T_2$  from the decaying exponential curve given in (4),  $T_{2new}$  is measured based on (10).

$$V_{TLnew} = V_F e^{-T_{2new}/(R_{ref} + R_x)C_{eq}}$$
(10)

Further, from (4) and (10), (11) is arrived at, as shown below.

$$T_{2new} - T_2 = (R_{ref} + R_x)C_{eq} \ln\left(\frac{V_{TL}}{V_{TLnew}}\right)$$
(11)

The selection of the threshold voltages  $V_{TL}$  and  $V_{TLnew}$  must be done such that,

- i) The difference between  $V_{TL}$  and  $V_{TLnew}$  is much higher than the typical noise level in both the signal  $v_{cf}$  and the input of the comparator.
- ii) The difference in the measured time coming from (9) and (11) is much greater than the resolution of the timer used.

Let  $T_{1new} - T_1 = \Delta T_1$  and  $T_{2new} - T_2 = \Delta T_2$ . Then, the difference between (11) and (9) gives (12).

$$\Delta T_2 - \Delta T_1 = R_{ref} C_{eq} \ln\left(\frac{V_{TL}}{V_{TLnew}}\right) \tag{12}$$

From (12),

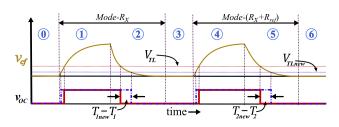


Fig. 4. Timing diagram illustrating the  $v_{cf}$  and  $v_{OC}$  waveforms, in Mode- $R_x$  and Mode- $(R_x + R_{ref})$ , using two threshold voltages  $V_{TL}$  and  $V_{TLnew}$ .

$$C_{eq} = \frac{\Delta T_2 - \Delta T_1}{R_{ref} \ln\left(\frac{V_{TL}}{V_{TLnew}}\right)}.$$
(13)

Thus, using the value of  $C_{eq}$  obtained from (13), the value of  $C_{12}$  can be obtained using (14).

$$C_{12} = \frac{C_{eq}C}{C - C_{eq}} \tag{14}$$

The above method to measure  $C_{12}$  is a simple, yet efficient one. According to (13),  $C_{eq}$  and hence  $C_{12}$  depend on  $V_{TL}$  and  $V_{TLnew}$ . A one-time correction to reduce the effect of the inaccuracy of  $V_{TL}$  and  $V_{TLnew}$  in  $C_{12}$  measurement is discussed in section III. A.

#### III. ERROR ANALYSIS

The important sources of error and their effects on the final output are discussed in this section. The non-idealities of the comparator, digital pins, accuracy of threshold voltages, and resolution of the timer are considered for the analysis.

#### A. Input Offset Voltage of Comparator and Threshold Voltage

Let  $V_{OS}$  be the input offset voltage of the comparator OC. In its presence, (3) and (5) become (15) and (16), respectively.

$$T_1 = R_x C_{eq} \ln\left(\frac{V_F}{V_{TL} \pm V_{OS}}\right) \tag{15}$$

$$T_2 = (R_{ref} + R_x)C_{eq}\ln(\frac{v_F}{v_{TL} \pm v_{OS}})$$
(16)

Dividing (16) by (15) gives the same equation as (6), from which  $R_x$  can be obtained as in (7). Thus, the presence of  $V_{OS}$  has no impact on the determination of  $R_x$  using the proposed scheme.

The same analysis holds good for the effect of variation in the threshold voltage. As can be seen in (15) and (16), any change in the threshold voltage comes as an addition or subtraction of  $V_{OS}$ . Since the measurement of  $R_x$  is not affected by the presence of  $V_{OS}$ , the inaccuracy of the threshold voltage does not affect the accuracy of the final output provided that it is stable within one measurement cycle.

As for the measurement of  $C_{12}$ , (12) becomes (17).

$$\Delta T_2 - \Delta T_1 = R_{ref} C_{eq} \ln \left( \frac{V_{TL} \pm V_{OS}}{V_{TLnew} \pm V_{OS}} \right)$$
(17)

The resulting capacitance obtained herein can be termed as  $C'_{eq}$ , and is given by (18). The corresponding coupling capacitance  $C'_{12}$  is given in (19).

$$C_{eq}' = \frac{C_{eq}}{1+\epsilon} \tag{18}$$

$$C_{12}' = \frac{C_{eq}C}{C(1+\epsilon) - C_{eq}} \tag{19}$$

In (18),  $C_{eq}$  is the capacitance in the absence of  $V_{OS}$  given by (13), and  $\epsilon = \ln \left[\frac{1 \pm V_{OS}/V_{TL}}{1 \pm V_{OS}/V_{TLnew}}\right] / \ln(V_{TL}/V_{TLnew})$ . For a typical  $V_{OS}$  of 10 mV, and threshold voltages of  $V_{TL} = 500$  mV and  $V_{TLnew} = 300$  mV, then  $\epsilon = -0.02$ . The value of  $(1 + \epsilon)$  can be estimated by performing an one-time measurement using a known value of  $C_{eq}$ . In the later measurements, the actual  $C_{eq}$  can be calculated as  $C'_{eq}$   $(1 + \epsilon)$ , and the same can be used to estimate  $C_{12}$  as in (14).

As mentioned above, the effect of change in the threshold voltage can be represented as an equivalent  $V_{OS}$ . Therefore, when we compensate for the effect of  $V_{OS}$  in the  $C_{12}$  measurement, the effect due to inaccuracy of the threshold voltages will also get corrected.

## B. Variation in the Voltage V<sub>DD</sub>

The dc voltage  $V_{DD}$  provided by a digital pin in HIGH state is not derived from a precision voltage source. Hence, variations are expected in it, e.g.,  $V_{DD} \pm \Delta v_{dd}$ , where  $\Delta v_{dd}$  is the variation. As one cycle measurement is completed in a very short time,  $\Delta v_{dd}$  is assumed constant during that cycle. In such a case, as  $V_F = (V_{DD} \pm \Delta v_{dd})C_{12}/(C_{12} + C)$ , (3) and (5) become (20) and (21), respectively.

$$T_{1} = R_{x} C_{eq} \ln \left[ \frac{(V_{DD} \pm \Delta v_{dd}) C_{12}}{V_{TL}(C_{12} + C)} \right]$$
(20)

$$T_{2} = (R_{ref} + R_{x})C_{eq} \ln \left[\frac{(V_{DD} \pm \Delta v_{dd})C_{12}}{V_{TL}(C_{12} + C)}\right]$$
(21)

As  $\ln \left[ \frac{(V_{DD} \pm \Delta v_{dd})C_{12}}{V_{TL}(C_{12}+C)} \right]$  is present in both the terms, (6) and the final output as per (7) are not affected.

As for the measurement of  $C_{12}$ , the variation in  $V_{DD}$  is irrelevant since the time differences as per (9), (11) and  $C_{12}$  as per (13) do not have the term  $V_{DD}$ .

#### C. Internal Resistance of the Digital Pins

Digital pins offering zero output resistance was assumed while explaining the operation in section II. To analyze the impact of the internal resistance  $R_i$  of the digital pin in the measurement of  $R_x$  and  $C_{12}$ , it is assumed that identical  $R_i$  is present at each digital input pin. In reality, there may be a mismatch of about a tenth of an ohm.

In the presence of  $R_i$ , the discharge time constant in mode-  $R_x$  is modified to  $\left(\frac{R_i(R_{ref}+R_i)}{R_{ref}+2R_i} + R_x\right)C_{eq}$ . However, since  $\frac{R_i(R_{ref}+R_i)}{R_{ref}+2R_i} \ll R_x$  in the prototype, the effects of  $R_i$  on the time measurement are expected to be negligible. In mode -  $(R_{ref} + R_x)$ , the discharge time constant becomes  $(R_{ref} + R_x + R_i)C_{eq}$ . Again, since  $R_i \ll (R_{ref} + R_x)$ , it can be neglected and thus does not significantly impact the time measurement. On the other hand, as long as the charging time is sufficiently longer (at least, five times higher) than the charging time constant, the charging phase is negligibly affected by  $R_i$ . Consequently, in the prototype developed, the measurement of  $R_x$  and  $C_{12}$  should not be affected by  $R_i$ .

#### D. Parasitic Capacitance of the Digital pins

Each digital pin, together with the PCB trace or cable to the sensor introduces a parasitic capacitance from the pin to ground. The capacitances  $C_{s1}$ ,  $C_{s2}$ , and  $C_{s3}$ , shown in Fig. 5, represent these parasitic capacitances, respectively, from node  $D_{IO-1}$ ,  $D_{IO-2}$ , and  $D_{IO-3}$  to ground. As these parasitic capacitances are in the range of a few pF, and the output resistance of the digital pins is in the range of tens of ohms, the discharge-time measurements in the mode- $R_x$  have negligible impact due to  $C_{s1}$  and  $C_{s2}$ . As  $C_{s3}$  is in parallel to C, the effective C will become  $C + C_{s3}$ , and  $C_{eq}$  will get modified as  $C_{eq}^{s3} = \frac{C_{12}(C+C_{s3})}{C_{12}+C+C_{s3}}$ . Since  $C_{eq}$  is not present in the estimation of  $R_x$  as in (7), there will be no effect.

In the case of mode- $(R_{ref} + R_x)$ , there are two discharging events happening simultaneously: (a) discharging of  $C_{s2}$ through  $R_{ref}$  to ground, and (b) discharging of  $C_{eq}^{s3}$  through  $(R_{ref} + R_x)$ . Since the time constant  $C_{s2}R_{ref} \ll C_{eq}^{s3}$  ( $R_{ref} + R_x$ ), it does not affect the discharging along the main discharge path (b). To keep the effect of  $C_{s3}$  very low, the condition  $C_{s3} \ll C$ should be ensured. As *C* is in nF and  $C_{s3}$  is in pF, the effect in  $C_{eq}$  and  $C_{12}$  is expected to be negligible.

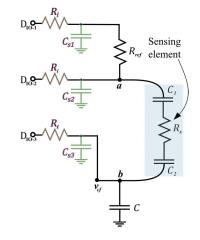


Fig. 5. The parasitic capacitances  $C_{s1}$ ,  $C_{s2}$ , and  $C_{s3}$  at the digital input pins.

#### E. Resolution of the Timer

The resolution of the timer embedded into the microcontroller plays an important role in the final accuracy. In the prototype developed, the timer has a resolution of 62.5 ns, and the time interval under measurement is in the range of a few hundred microseconds for the  $R_x$  measurement. In the case of  $C_{12}$  measurement, the time difference estimated as per (9), (11) is in the range of tens of microseconds. Therefore, the limited resolution of the timer will have more effect on the accuracy of the  $C_{12}$  measurement. To achieve a higher accuracy, a timer with a higher resolution is recommended.

#### IV. EXPERIMENTAL SETUP AND RESULTS

The hardware prototype of the proposed scheme, presented in Fig. 1(b), was realized in two steps, for the measurement of both  $R_x$  and  $C_{12}$ . In the first step, a prototype was developed to

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test the feasibility of the approach without using a microcontroller. For this, a data acquisition system (DAS), namely Analog Discovery kit (14-bit resolution, 100 MS/s sampling rate) from Digilent, and a Virtual Instrument (VI), developed using NI LabVIEW, were used. The function generator, included in the DAS, was programmed to generate the HIGH and LOW state signals as required in the two modes of measurement given in Table-I and Fig. 2. As indicated in Table-I, D<sub>IO-1</sub> can be in HZ/high in state-1, and HZ/low in state-2. During the experimental tests it was kept in high during state-1 and low in state-2. In this DAS, the operation of the function generator is independent of the other operations, e.g., digitization, data transfer, etc. Thus, the signal at node 'b' was digitized using the DAS and analyzed, as described below. In the second step, a microcontroller-based prototype was developed and tested. In this case, the two modes of measurement were realized by setting the microcontroller pins in accordance with Table-I and Fig. 2. The components used to build the prototypes are listed in Table - II. A photograph of the experimental setup is shown in Fig. 6.

Table II PROTOTYPE

Component	Part/Value	Component	Part/Value
$V_{DD}$	5 V	DAS	Analog Discovery kit
Opamp	LF357	С	1 nF
microcontroller	ATMEGA328	R <sub>ref</sub>	50.09 kΩ

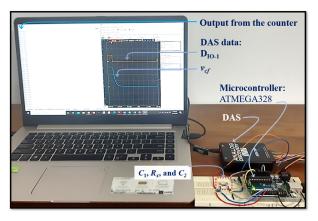


Fig. 6. Photograph of the experimental set-up.

# A. Measurement of $R_x$

#### 1) DAS-based Prototype

In order to assess the practicality of the measurement scheme, first an experimental set-up was developed in such a way that its performance was not limited by the non-idealities of the microcontroller. For this, the functionality of the proposed circuit given in Fig. 1(b) was realized in hardware with the help of a DAS.

First, the mode -  $R_x$  was implemented. For this, the function generator was set to give a square wave (with a frequency of 200 Hz, amplitude of 5 V, and an offset of 2.5 V) emulating the function of pin D<sub>IO-1</sub>. In addition,  $R_{ref}$  was shorted with a wire

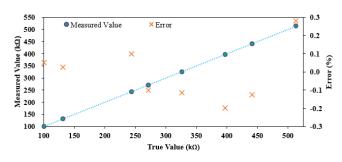


Fig. 7. Results from the prototype for a range of  $R_x$ . The output and the error obtained are shown.

in order to measure only  $R_x$ . Node 'b' was connected to the input of a voltage follower, built using a very low bias current opamp (LF357), whose output was given to the DAS. The analog channels of the DAS were used to acquire the important waveforms. The connections to the DAS were made such that channel-1 captured the signal at D<sub>IO-1</sub>, whereas channel-2 captured  $v_{cf}$  at the output of the voltage follower. To operate in mode - ( $R_x + R_{ref}$ ), the short across  $R_{ref}$  was removed. The developed system enables performing mode -  $R_x$  and mode - ( $R_x + R_{ref}$ ) independently and acquires the corresponding  $v_{cf}$  waveforms.

A VI was accordingly developed, using LabVIEW, to acquire the signals from the DAS and process them, as described further. For a chosen value of  $R_x$ , the  $v_{cf}$  data during the discharge phase was analyzed. The VI was programmed to perform an exponential fit of the  $v_{cf}$  data and provide the parameters representing the decaying exponential curve to compute  $T_1$ . For this computation,  $V_{TL} = 300$  mV. Then, the  $v_{cf}$  data corresponding to mode - ( $R_x + R_{ref}$ ) was taken and processed to compute  $T_2$  for the same  $V_{TL}$ . Once  $T_1$  and  $T_2$  were computed, the value of  $R_x$  was calculated by the VI using (7). This experiment was repeated for a range of  $R_x$  from 100 k $\Omega$  to 500 k $\Omega$ , in several steps. Later, this study was repeated for 50 k $\Omega$ , 700 k $\Omega$  and 800 k $\Omega$  and the accuracy was within the same range. The coupling capacitors used in this study were  $C_1$ =  $C_2$ = 4 nF, thus resulting in  $V_{CF}$ = 3.3 V. In each measurement, the actual value of  $R_x$  was measured using a 6-1/2 digit multimeter (34410A, from Agilent Technologies) with an accuracy of  $\pm (0.012\%$  of the reading  $\pm 0.001\%$  of the chosen range). This was used to compute the error introduced by the proposed method. The results are shown in Fig. 7. The maximum error (expressed as a percentage of the full scale, %FS) noted during the experiments was 0.28%, indicating the high accuracy of the proposed approach, although its simplicity.

As mentioned in section I, for the potential tap water conductivity monitoring in the range of 50-800  $\mu$ S/cm [12], the resistance of the water column of interest lies in the range between 50 k $\Omega$  and 750 k $\Omega$ ; this is assuming a distance between the electrodes of 12 cm and an inner diameter of the insulating tube of 6.35 mm. The range can be adjusted by adjusting the distance between the electrodes. As described above, to study the performance of the proposed measurement scheme, the value of  $R_x$  was incremented in several steps within that range. For low values of  $R_x$  (say, lower than 1 k $\Omega$ ) with a coupling

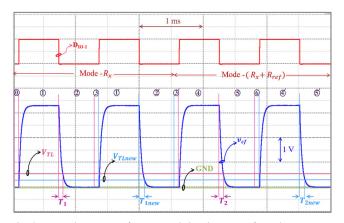


Fig. 8.  $v_{cf}$  and D<sub>10-1</sub> waveforms recorded, using DAS, from the prototype during the measurement process. States 0 to 6 correspond to  $R_x$  measurement while 1', 2', 3', 4', and 5' correspond to the cycles used for  $C_{12}$  measurement.

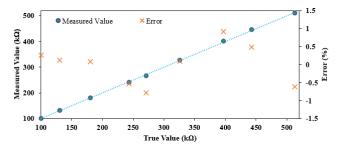


Fig. 9. Results from the prototype-2 for a range of  $R_x$ . This prototype was implemented using microcontroller.

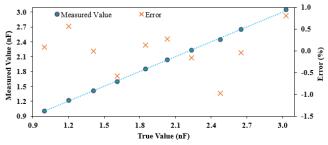


Fig. 10. Results from the DAS based prototype for a range of  $C_{12}$ .

capacitance of 2 nF [1], the discharge time will be very short and the resolution of the timer embedded into the ATMEGA328 would not be sufficient to accurately measure it. Thus, for the proposed design using the ATMEGA328, the range of  $R_x$ should be such that the discharge time is sufficiently long for the embedded timer to measure accurately. Once the range of  $R_x$  is fixed,  $R_{ref}$  is chosen so that  $(T_2 - T_1)$  is much longer than the timer resolution. In addition,  $R_{ref}$  is set lower than  $R_x$ , to avoid an unnecessary increase of the overall measurement time. 2) Microcontroller-based Prototype

In this experiment, instead of using the DAS and the VI as before, the scheme was entirely realized using a microcontroller as given in Fig. 1(b), supplied at 5 V. The signal  $v_{cf}$  was given to node A<sub>1</sub> of the microcontroller, and the built-in timer was programmed to measure, with a timing resolution of 62.5 ns, the time intervals  $T_1$  and  $T_2$  during the corresponding states '2'

and '5', respectively, as given in Table-I. The important waveforms were recorded using the oscilloscope in Analog Discovery kit, for the charge and discharge cycles for mode -  $R_x$  followed by mode - ( $R_x + R_{ref}$ ), as shown in Fig. 8; note that states 1' to 5' in Fig. 8 correspond to  $C_{12}$  measurement presented later in section IV.B. The experiment was repeated for the same set of values of  $R_x$  employed in section IV-A.1. The value of  $R_x$  in each case, and the corresponding error in %FS, was computed and is presented in Fig. 9. The maximum error noted during the experiments was 0.91%. The output of this experiment, in comparison with that from section IV-A.1, reflects the effect of the non-idealities of the components and ICs used in the microcontroller-based prototype.

# *B. Measurement of* $C_{12}$

#### 1) DAS-based Prototype

This testing approach is similar to the one used to prove the feasibility to measure  $R_{x}$ . In this study, the signal at D<sub>IO-1</sub> was generated using the function generator in the Analog Discovery kit, serving as DAS. This signal and  $v_{cf}$  at node b were recorded using the same DAS. This data was provided to the VI developed in section IV.A.1, to perform an exponential fit of the  $v_{cf}$  data and provide the parameters. It was modified to compute the time intervals  $T_1$  and  $T_{1new}$  for  $V_{TL} = 500$  mV and  $V_{TLnew} = 300$  mV, respectively. Afterwards, using the same procedure,  $T_2$  and  $T_{2new}$  were determined. Once the time values were computed, the value of  $C_{12}$  was calculated by the VI using (14). In these tests,  $R_x$  was kept constant at 100 k $\Omega$ and  $C_{12}$  was varied in steps of 200 pF within the range from 1 nF to 3 nF. The actual values were measured using an LCR meter (HP 4274A) that has an accuracy of 0.1%. The results obtained are shown in Fig. 10. The maximum error, in full scale, noted during the experiments was -0.96%.

# 2) Microcontroller-based Prototype

To perform and verify the accuracy of the microcontrollerbased measurement of  $C_{12}$ , as presented in section II. B., the program for the prototype developed using the ATMega328 microcontroller to test the measurement of  $R_x$  was modified. A new cycle was included in each of the modes, to measure  $C_{12}$ , along with  $R_x$ . Thus, now there are four time measurements, two in each of the two modes of operation. The operations of the digital pins  $D_{IO-1}$ ,  $D_{IO-2}$ , and  $D_{IO-3}$  are identical in both the cycles present in the same mode. The only change is in the threshold ( $V_{TLnew}$ ) used in the new cycles. The following modes were executed: mode- $R_x$  with  $V_{TL} = 500$  mV, followed by the

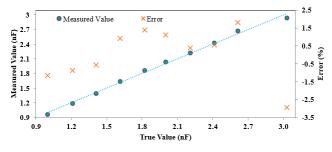


Fig. 11. Results from the microcontroller-based prototype, for the measurement of  $C_{12}$ .

same mode with  $V_{TLnew}$  = 300 mV, and then mode-( $R_x + R_{ref}$ ) with  $V_{TL}$  = 500 mV, followed by with  $V_{TLnew}$  = 300 mV, as shown in Fig. 8. This was repeated for the same range of  $C_{12}$  as in the DAS-based prototype. From the measured discharging times, the value of  $C_{12}$ , in each case, was estimated using (14). The corresponding error in each measurement was computed, and presented in Fig. 11. As can be seen, the maximum error noted during the experiments was -2.94% (full scale). Based on the results given in section IV.B.1, one can conclude that an improvement in the accuracy of the results is possible with a higher-end microcontroller.

#### C. Noise and Resolution Study

An experimental study was conducted to evaluate the noise and resolution in the measurements carried out by the microcontroller-based prototype. In this study,  $R_x$  and  $C_{12}$  were set to 100 k $\Omega$  and 1 nF, respectively, and 200 readings were recorded in a continuous manner. From this data, the signal to noise ratio (SNR), standard deviation (SD),  $\sigma$ , effective number of bits (ENOB), and repeatability, d, were computed using the formulae given in Table III and presented in the same table. Accordingly, a resolution of 8 bits is feasible for the measurement of both  $R_x$  and  $C_{12}$  which is a remarkable value considering the simplicity of the proposed circuit and the complexity of the sensor circuit model.

The repeatability experiment was carried out for several resistance values between 50 k $\Omega$  and 800 k $\Omega$ . The output was

TABLE III NOISE AND RESOLUTION STUDY

Parameter and Formula	$R_x$	C <sub>12</sub>
$SNR = 10 \log \frac{\sum_{i=1}^{M} X(i)^2}{\sum_{i=1}^{M} [X(i) - \bar{X}]^2}$	50.47 dB	51.1 dB
Standard deviation, $\sigma = \sqrt{\frac{\sum_{i=1}^{M}  X(i) - \bar{X} ^2}{M-1}}$	0.30 kΩ	5.94 pF
$ENOB = \frac{(SNR-1.76)}{6.02}$	8.1 bits	8.2 bits
Repeatability, $d = \frac{\Delta_{rmax}}{X_u - X_l} \times 100\%$	0.51%	1.13%
Trans all and the second se		

 $X(i) = i^{\text{th}}$  measurement;  $\overline{X}$  = average value of the measured data

M =total number of measurements

 $X_u, X_l$  = upper and lower limits of the measured data

 $\Delta_{rmax}$  = maximum difference between repeated measurements

TABLE IV						
NDADD DEVIATION (CD) FOD A NUDE DANCE OF D						

_	STANDARD DEVIATION (SD) FOR A WIDE RANGE OF $R_x$									
1	$R_x [k\Omega]$	50	100	200	300	400	500	600	700	800
S	SD [kΩ]	0.303	0.303	0.304	0.305	0.304	0.305	0.305	0.301	0.305

TABLE V	
OD FOD 1001 O MEAGUDEMENT	AT 10 DITEDUAL C

SD FOR 100 KG2 MEASUREMENT AT 12 INTERVALS								
Time [hr]	1	2	3	4	5	6		
$SD[k\Omega]$	0.302	0.301	0.304	0.302	0.304	0.304		
Time [hr]	7	8	9	10	11	12		
$SD[k\Omega]$	0.304	0.305	0.303	0.300	0.301	0.301		

recorded for a data length of a hundred measurements, for each resistor, and the corresponding SD was computed. As presented in Table IV, SD was very similar for the different resistance values under test. Later, the experiment was continued for the 100 k $\Omega$  case for 12 hrs, with the aim of testing any potential time drifts. In this, the microcontroller was powered all the time and a hundred measurements recorded for every one hour. The corresponding computed SD is presented in Table V. Again, the values of SD of the measured outputs remained very similar through the course of the day.

## V. DISCUSSION

When a series combination of resistance and capacitance needs to be measured, a widely used method, apart from the conventional bridge-based one [6] that requires manual balancing, is synchronous demodulation [24]. This requires a precision sine wave excitation, multipliers, low-pass filters, followed by a digitizer and a processor for computation. A slightly optimized version of this mechanism is available in [25]. It is still complex, as it requires an on-board digital signal processing engine to compute discrete Fourier transform of the digitized signal. The synchronous demodulation approach gives high accuracy, but it is complex considering the number of individual units required to implement the analog multiplication, and the overall power consumption. A scheme based on a modified Martin oscillator is presented in [26] for impedance measurement. This requires a precision current source. The oscillator has to be operated in four different modes, and the values of resistance and capacitance are computed from the four oscillator time periods measured, one from each mode. This method is complex and time-consuming compared to the direct-microcontroller approach presented in this paper. On the other hand, there are several schemes available for the measurement of capacitance [27], [28] and resistance [6], [29] but none of these are useful to measure the resistance of a capacitively-coupled resistive sensor.

In the literature, there are direct-microcontroller interfaces for the measurement of a single component: resistance in [15]-[18], [30], [31], [32], capacitance in [19], [20], and inductance in [21], [22]. In [33] both resistance and capacitance were measured, but for a lossy capacitive sensor where resistance and capacitance are in parallel.

The capacitively-coupled resistive sensor has not been explored so far from the perspective of designing a directmicrocontroller interface. A previous work [23] by the authors deals with a suitable circuit for such a sensor, but it was designed to measure only the resistance. In some applications, however, the value of the capacitance that is in series with the resistive sensor carries useful information. Therefore, it will be advantageous if the same interface could measure both resistance and capacitance. In this context, it is for the first time that a direct-microcontroller interface able to measure both components in a capacitively-coupled resistive sensor has been developed and evaluated. The proposed method of estimating the coupling capacitance  $C_{12}$  does not require any additional hardware compared to the initial study presented in [23]. Since

TABLE VI
COMPARISON

COMPARISON							
Ref.	Type of sensor		Conversi- on time	Resolu- tion	Accura -cy [%]		
[15]	Single resistive ser	nsor	≈50 ms	0.1 Ω	0.85 <sup>(a)</sup>		
[17]	Single resistive ser with lead wire resist		2.58 ms	$0.12 \ \Omega^{(b)}$	0.59 <sup>(c)</sup>		
[19]	Single capacitive se	ensor	1.5 s	NS	0.8		
[20]	Differential capaci sensor	tive	50 ms	7 bits	1.1 <sup>(d)</sup>		
[33]	Resistance and capacitance connection in parallel		≈3 ms	NS	1		
This work <sup>(e)</sup>	$R_x$ and $C_{12}$ connected in	R <sub>x</sub>	5 ms	8.1 bits	0.91 <sup>(f)</sup>		
WOIK	series	$C_{12}$	5 ms	8.2 bits	2.94 <sup>(f)</sup>		
<sup>(a)</sup> After a calibration, it was reduced to 0.01%. <sup>(b)</sup> Standard deviation.							

<sup>(c)</sup> Hysteresis error. NS-Not studied. <sup>(d)</sup>Nonlinearity error. <sup>(e)</sup>[23] deals with  $R_x$  alone, not measurement of  $C_{12}$ . <sup>(f)</sup>0.28% (resistance) and 0.96% (capacitance) for DAS-based method.

the direct microcontroller interface is simple, robust, low-cost, and consumes less power [34], it offers the opportunity to realize portable and scalable measurement systems that can be widely used for this type of sensor.

As presented in Table VI, the new scheme offers a performance which is almost on par with the schemes (without special calibration) available to individually measure resistance and capacitance, whilst providing a direct-microcontroller method to measure both when connected in series. This functionality is achieved without additional components or parts in the measurement system.

#### VI. CONCLUSION

proposed direct microcontroller interface The for capacitively-coupled resistive sensors, measuring both the resistance and the coupling capacitance, has been attempted for the first time. The measured resistance output is independent of the values of the coupling capacitances, the charging capacitor, the dc excitation voltage, and the preset threshold voltage. When a resistive sensor is coupled to the measurement system through capacitances, one of the main challenges is to make the resistance measurement insensitive to variations in the coupling capacitances, which is achieved herein without using the synchronous demodulation technique adopted by conventional approaches. The proposed circuit is simple in design and completes both measurements in 10 ms. The feasibility study of the proposed circuit for resistance and coupling capacitance measurement, exhibits a maximum error of 0.28% and 0.96%, respectively, when evaluated without considering the nonidealities of the microcontroller. In the practical setup, maximum error noted for resistance measurement is 0.91%, and that for the coupling capacitance is 2.94%. Higher accuracy can be achieved by selecting a microcontroller with more appropriate specifications. The proposed scheme uses only a simple microcontroller and a few passive components and hence can realize a low-cost, low-power measurement system.

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