Voltage Sensorless Control Scheme Based on Extended-State Estimator for a Grid-Connected Inverter

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Abstract- This paper presents a robust voltage sensorless control scheme to eliminate the need of line-voltage sensors in an LCL-filtered grid-connected inverter system. The proposed control scheme is based on an extended-state estimator structure which consists of a state feedback regulator and an estimator. The main contribution of this study relies on the utilization of the linear matrix inequality approach and the internal model principle to synthesis a robust control scheme that equips a grid-connected inverter with the ability to withstand grid disturbances. As opposed to the existing works, the proposed control scheme can simultaneously deal with all types of grid disturbances such as grid voltage distortion, grid voltage imbalance, and grid impedance variations. The effectiveness of the proposed controller is highlighted through comparative simulation results and in-depth analyses. Experimental results are also given to validate the practicality of the proposed control scheme.

I. INTRODUCTION

Recently, grid-connected inverters have been increasingly employed in distributed power generation systems (DPGS) to deliver renewable energy to the utility grid [1], [2]. Despite the fact that inverters are energy-efficient and fully controllable, many sensing devices such as the grid voltage and grid-side current sensors are required to ensure a stable inverter operation [3]. Particularly, for LCL-filtered inverters, additional sensors to measure capacitor voltage and/or inverter-side current are often employed to cope with the potentially unstable dynamics caused by LCL filter [4], [5]. This increases the cost and complexity of inverter-based DPGS [6].

Due to the better harmonic attenuation capability and smaller physical size, LCL filters are often chosen over the traditional L filters in recent years. However, the resonant characteristic of LCL filter adversely influences the performance and stability of inverter-based systems [7]. To address this problem, several damping methods have been suggested in the literature [7]-[16]. These methods fall into two main groups which are passive damping and active damping. As active damping methods are more flexible and cost-effective, they are more preferable and widely used [7]. The potential resonance caused by LCL filters can be actively damped by a full state feedback controller [8], [9]. To reduce the number of sensors, the approaches which use only the information on grid-side current or inverter-side current have been proposed in [10], [11] for the purpose of controlling the injected current stably. As another approach, the works in [12] and [13] proposed a state feedback design which guarantees the robustness of the closed-loop system by using the linear matrix inequality (LMI) approach. Other studies use the pole placement approach [14] or the concept of virtual resistor [15], [16] to deal with the inherent instability of LCL filters. Despite the fact that these aforementioned control approaches can guarantee a stable operation of LCL-filtered inverters, they still need voltage sensors for the purpose of grid synchronization. To eliminate the need of grid voltage sensors, several line-voltage sensorless control schemes have been proposed [6], [17]-[24]. In these studies, estimators are often employed to predict the grid angular frequency and phase angle. These estimators are mainly achieved either by adaptive approaches or linear approaches. In the former case, the grid voltage is predicted by using the estimators based on the adaptive algorithms [6], [17]-[20]. In [6], a predictive controller is used to handle the injected current and an adaptive voltage estimation is used to predict the grid voltage. A control scheme which is based on the Kalman filter and sliding mode control is presented in [18]. In this work, the Kalman filter is used to observe the system states including grid voltage, and then, three decoupled sliding mode controllers (SMCs) use these estimated variables to control the inverter. As another approach, the literature [19] presents a line-voltage sensorless control scheme which is accomplished by a deadbeat control and an adaptive estimator. Because this estimator predicts not only grid voltage but also model uncertainties, a considerable performance improvement under grid voltage distortions and grid impedance variations can be observed. However, it is worth mentioning that the
nonlinearity nature of adaptive algorithms makes it difficult to ensure and analyze the robustness of the overall system.

The latter case of line-voltage sensorless control scheme is based on linear estimators. Even though the use of a linear estimator can greatly facilitate the design and analysis of control system, only limited researches are dedicated to this area [21]-[24]. In [22], a change of variables is employed to construct an extended-state estimator. As a result of using a new set of variables, the observer dynamics and inverter parameter uncertainties can be dealt with separately. The presented experimental results prove the validity of this control scheme under the ideal voltage condition. As a different approach, virtual flux-based estimation method is proposed in [24]. Despite having considerably good performance, these approaches only address partially the issues related to voltage sensorless control of LCL-filtered inverter.

Considering the limitations of the aforementioned approaches, extended-state estimator appears to be a promising solution. Extended-state estimator utilizes internal model principle (IMP) to achieve asymptotic reference tracking and asymptotic disturbance rejection [25]. By means of using system model combined with known characteristics of disturbance, the external disturbance can be estimated. Then, this estimated disturbance can be feedback to the system to cancel out the effect of actual disturbance. Due to the effectiveness for system with limited measurement devices, the extended-state estimator has been widely used for control design and fault diagnosis [26], [27]. Recently, extended-state estimator has been proved to be feasible for voltage sensorless control of a grid-connected inverter [23]. In [23], an extended-state observer is used to estimate grid voltage for the current control loop. Besides good performance under unbalanced grid condition, the study in [23] also reveals that the order of the closed-loop system is directly proportional to the order of the disturbance model. Thus, incorporating the harmonic terms of grid voltage into the disturbance model increases the order of the closed-loop system significantly, which poses considerable difficulty in obtaining controller and estimator gains.

Aiming to propose a complete control design framework, this paper presents a robust control scheme for an LCL-filtered grid-connected inverter with only the grid-side current measurements. Being constructed by a state feedback regulator and an extended-state observer based on the IMP, the proposed control scheme is able to effectively suppress the grid voltage disturbance such as harmonic distortions and imbalance. Moreover, a systematic design procedure based on the LMI is proposed to obtain robust and optimal gains, which ensures a stable operation of the inverter under grid impedance variations. The novelty of this paper includes the utilization and the combination of different control design approaches to achieve a robust optimal controller for grid-connected inverter with only grid-side current feedback. In particular, the extended-state estimator-based control structure, IMP, and LMI-based design approach are combined into one complete control scheme to ensure a fast response to reference, and robustness against distorted and unbalanced grid voltage as well as grid inductance variation. To the best of the authors’ knowledge, such a control scheme has not been reported in the literature yet. Additionally, theoretical analyses as well as the simulation and experimental results are given to prove the usefulness of the proposed control scheme.

II. SYSTEM MODELING

Fig. 1 shows the power circuit of a three-phase grid-connected inverter with LCL filters, where \( R_1, R_2, L_1, \) and \( L_2 \) represent the resistances and inductances of the filters, respectively, \( C_f \) represents the filter capacitance, and \( L_g \) represents the grid inductance. Since the voltage equations in the \( \alpha \)-axis and \( \beta \)-axis are independent of one another, it is reasonable to design a control scheme for one axis, and then, to apply it to the other.

From Fig. 1, the state-space equations of the inverter system can be given in the stationary reference frame as

\[
\dot{x}(t) = A \cdot x(t) + B \cdot u(t) + B \cdot w(t) \\
y(t) = C \cdot x(t)
\]

where \( x = [i_c \ i_v \ i_g]^T \) with \( i_c, v_c, \) and \( i_g \) being the inverter-side current, the capacitor voltage, and the grid-side current, respectively, \( u = v \) is the inverter output voltage, \( w = e \) is the grid voltage, and the parametric matrices are given by

\[
A_c = \begin{bmatrix}
-R/L_1 & -1/L_2 & 0 \\
1/C_f & 0 & -1/C_f \\
0 & 1/(L_2 + L_g) & -R/(L_2 + L_g)
\end{bmatrix}, \\
B_c = \begin{bmatrix}
1/L & 0 \\
0 & 0 \\
0 & -1/(L_2 + L_g)
\end{bmatrix}
\]

For the purpose of designing a discrete controller, it is necessary to discretize the inverter system in (1) and (2). Generally, the input of the inverter system has the form of a staircase signal with sampling period \( T_s \). Therefore, zero-order-hold technique can be used to obtain exact discretization of (1) and (2) in the time domain as [28]

\[
\begin{bmatrix}
x_{d0}(k+1) \\
x_d(k+1)
\end{bmatrix} = \begin{bmatrix} A_{d0} & B_{d0} \\
0 & 0 \end{bmatrix} \begin{bmatrix} x_{d0}(k) \\
x_d(k)
\end{bmatrix} + \begin{bmatrix} 0 \\
1 \end{bmatrix} u(k) + \begin{bmatrix} B_{d0} \\
0 \end{bmatrix} w(k)
\]

![Fig. 1. Power circuit of a three-phase inverter with LCL-filters.](image-url)
The extended-state estimator as well as grid disturbance rejection capability without using any voltage sensors. For this purpose, the disturbances, which are the grid voltage and parameter variation, are estimated and fed back to the inverter system as a compensating control input to cancel their effect. While these disturbances can be only achieved by the digital implementation. The system in (4) and (5) can be expressed in a compact form as

\[ x(k + 1) = Ax(k) + Bu(k) + B_d w(k) \]  \hspace{0.5cm} (6)
\[ y(k) = Cx(k) \]  \hspace{0.5cm} (7)

**III. PROPOSED VOLTAGE SENSORLESS CONTROL SCHEME**

This section presents the proposed control scheme. Fig. 2 shows the block diagram of the proposed control scheme which mainly consists of a state feedback regulator and an extended-state estimator. First, the design procedure for the state feedback regulator based on the LMI approach is given. Then, the extended-state estimator as well as grid synchronization is explained.

**A. State Feedback Regulator**

As the grid impedance varies, the parametric matrices in (3) vary accordingly. As a result, the system in (6) and (7) includes uncertain parameters. According to [29], Equation (6) can be expressed in a polytopic form as

\[ x(k + 1) = A(\alpha)x(k) + Bu(k) + B_d(\alpha)w(k) \]  \hspace{0.5cm} (8)

where

\[ (A, B_d)(\alpha) = \sum_{j=1}^{2} \alpha_j (A_j, B_{dj}) \]  \hspace{0.5cm} (9)

The vertex matrices in (9) are obtained by evaluating the matrices \((A, B_d)(\alpha)\) for the maximum and minimum values of \(L_\alpha\).

A state feedback regulator is introduced to stabilize the inverter system considering significant variations of grid impedance during operation. For the given system in (7) and (8), a state feedback regulator can be given as

\[ u(k) = Kx(k) \]  \hspace{0.5cm} (10)

The closed-loop system using the state feedback regulator can be obtained by substituting (10) into (8) as

\[ x(k + 1) = (A(\alpha) + BK)x(k) + B_d(\alpha)w(k) \]  \hspace{0.5cm} (11)
\[ y(k) = Cx(k) \]  \hspace{0.5cm} (12)

The system in (11) is quadratically stable if, and only if, the following inequalities hold as [30]

\[
\left[ \begin{array}{cc}
\varepsilon^2Q & -QYK \\
AQ + BY & -YQ
\end{array} \right] > 0 \quad \text{and} \quad Q > 0
\]  \hspace{0.5cm} (13)

where \(Q \in \mathbb{R}^{4x4}\) is a positive-definite matrix, \(\varepsilon \in (0, 1)\) is the decay rate which determines the convergent speed of the system states and \(Y = KQ\).

Robust and optimal feedback gain \(K\) for a chosen decay rate can be obtained by solving the following LMI optimization problem:

\[
\begin{array}{l}
\text{Minimize} \quad Q \\
\text{subject to} \quad (13).
\end{array}
\]  \hspace{0.5cm} (14)

In (14), \(A_j\) is obtained by using (11) and (12), and \(\varepsilon\) is chosen decay rate. The variables \(Y\) and \(Q\) can be calculated by a LMI solver such as LMI control Toolbox in MATLAB [31]. Then, the feedback gain vector can be evaluated as \(K = Y^{-1}Q\).

**B. Extended-state Estimator**

The main objective of constructing an extended-state estimator is to achieve the tracking performance as well as disturbance rejection capability without using any voltage sensors. For this purpose, the disturbances, which are the grid voltage and parameter variation, are estimated and fed back to the inverter system as a compensating control input to cancel out their effect. While these disturbances can be only compensated by applying an appropriate control input \(u(k)\), both \(B_d(\alpha)\) and \(w(k)\) in (6) have uncertainties. Therefore, it is necessary to introduce an equivalent component which can be applied at the control input to remove the effect on injected current by the actual disturbances [28]. As a result, applying the negative of the equivalent disturbance at control input will cancel out the effect of grid voltage and parameter variation on the system output, which is the injected current. Considering the equivalent disturbance, (8) can be rewritten as

\[ x(k + 1) = A(\alpha)x(k) + B(u(k) + w_e(k)) \]  \hspace{0.5cm} (15)

where 

\[ w_e(k) = \sum_{j=1}^{2} \alpha_j w(k + 1) \]  \hspace{0.5cm} (16)
where $w_e$ is the equivalent disturbance.

At this point, it is worth noting that although the magnitude and phase of $w_e$ are generally unknown, its frequency is identical with grid voltage frequency, as a property of a linear time-invariant system. For a three-phase system, the dominant harmonics are in frequencies of $(6k \pm 1)^{th}$ with $k$ being a non-negative integer. Since both the fundamental and harmonic voltages of the grid are sinusoidal functions of time, they can be modeled as

$$
\begin{align*}
\dot{x}_{we}(t) &= F_{el} x_{we}(t) \\
W_{we}(t) &= H x_{we}(t)
\end{align*}
$$

where $x_{we} \in \mathbb{R}^{24}$ being the state vector of disturbance model

$$
F_{el} = \begin{bmatrix} 0 & 1 \\ -\omega_i^2 & 0 \end{bmatrix}, \quad H_i = \begin{bmatrix} 1^T \\ 0 \end{bmatrix}
$$

and the subscript $i$ indicates the order of the component whose angular frequency is $\omega_i$. By using the state-space model in (16) and (17), arbitrary harmonically distorted grid voltage can be represented as

$$
\begin{align*}
\dot{x}_{we}(t) &= F_{el} x_{we}(t) \\
W_{we}(t) &= H x_{we}(t)
\end{align*}
$$

where

$$
x_{we} = \begin{bmatrix} x_{we1} \\ \vdots \\ x_{we24} \end{bmatrix}, \quad F_e = \begin{bmatrix} F_{el} \\ \vdots \\ \vdots \\ F_{el} \end{bmatrix}, \quad H = \begin{bmatrix} H_i^T \\ \vdots \\ \vdots \\ H_i^T \end{bmatrix}.
$$

The discrete-time representation of (19) and (20) can be given as

$$
\begin{align*}
x_{we}(k+1) &= F_d x_{we}(k) \\
W_{we}(k) &= H x_{we}(k)
\end{align*}
$$

where $F_d = e^{F_d T_i}$. To construct the extended-state estimator, the grid voltage model in (22) and (23) can be augmented with the inverter model in (7) and (15) as

$$
\begin{align*}
\begin{bmatrix} x(k+1) \\ \dot{x}_{we}(k+1) \end{bmatrix} &= \begin{bmatrix} A(\alpha) & BH_x \\ 0 & F_d \end{bmatrix} \begin{bmatrix} x(k) \\ x_{we}(k) \end{bmatrix} + \begin{bmatrix} B \\ 0 \end{bmatrix} u(k) \\
y(k) &= [C \ 0] \begin{bmatrix} x(k) \\ x_{we}(k) \end{bmatrix}.
\end{align*}
$$

Equations (24) and (25) can be concisely expressed as

$$
\begin{align*}
x_e(k+1) &= A_e(\alpha)x_e(k) + B_e u(k) \\
y(k) &= C_e x_e(k).
\end{align*}
$$

The estimator which predicts the system states and grid voltage from inverter reference voltage $u(k)$ and grid-side current measurement $y(k)$ can be given as

$$
\begin{align*}
\bar{x}_e(k+1) &= A_e(\alpha)\bar{x}_e(k) + B_e u(k) + L(C_e \bar{x}_e(k) - y(k)) \\
\bar{x}_e(k+1) &= (A_e(\alpha) + LC_e)\bar{x}_e(k).
\end{align*}
$$

For the purpose of obtaining a robust and optimal $L$, the procedure used to design $K$ in the previous section is applied to (29). The stability condition for the estimator in (28) can be given as

$$
\begin{align*}
\varepsilon_e^2 Q_e &> 0 \\
\epsilon Q(A_e + Y C_e)^T Q &> 0 \quad \text{subject to (30)}.
\end{align*}
$$

Then, the estimator gain is $L = Q_e^{-1}Y_e$. Even though the regulator and the estimator are individually stable, the closed-loop stability is not guaranteed as a result of uncertain parameters. Moreover, it is impossible to implement an estimator with varying parameters included in (28). Therefore, it is desirable to find fixed parametric matrices for the estimator, which together with $K$ and $L$, guarantees the stability of the closed-loop system. For this purpose, the closed-loop system which includes the regulator, the estimator, and the feedback grid voltage $\bar{V}(k)$ can be derived from (10), (15), (28), and Fig. 2 as

$$
\begin{align*}
\begin{bmatrix} x(k+1) \\ \dot{x}_{we}(k+1) \end{bmatrix} &= \begin{bmatrix} A(\alpha) & B(K_e - K_c) \\ -LC_e A_e + B_e(K_e - K_c) + LC_e \end{bmatrix} \begin{bmatrix} x(k) \\ x_{we}(k) \end{bmatrix} \\
+ &\begin{bmatrix} B \\ B \end{bmatrix} N r(k) + \begin{bmatrix} B \\ 0 \end{bmatrix} w_e(k).
\end{align*}
$$

where $K_e = [K \ 0]$, $K_c = [0 \ H]$, $r$ is the reference current, and $N$ is a feedforward gain.

The proposed complete design procedure including the previous design process to determine the set of stable estimator parameters is shown in Fig. 3. It is worth noticing that the grid inductance $L_g$ serves as a virtual value to check the stability of the system in (32) on each iteration. By using the proposed procedure, the values for $L_g$ which results in a stable closed-loop system can be determined. The underlying
principle of the proposed procedure is based on the stability conditions of a discrete-time polytopic uncertain linear time-invariant system [32]. These conditions state that the system in (32) is robustly stable in the uncertain domain (12) if, and only if, the stability is guaranteed at the vertexes of the uncertain domain. As a result, for each valid \( L_{ge} \), an estimator which results in a stable closed-loop system can be constructed.

To demonstrate the proposed procedure, a regulator and an extended-state estimator are designed for a three-phase inverter system whose parameters are given in Table I. For the given system, the grid voltage is assumed to be polluted with 5th, 7th, and 11th harmonics. To accomplish the first two steps of the procedure, the decay rates are chosen as \( \varepsilon = 0.6 \) and \( \varepsilon_g = 0.985 \). In order to choose proper decay rates, it is worth bearing in mind that the convergent speed of system states is increased as the decay rate is decreased. On the other hand, the stability margin is decreased as the decay rate is decreased. As a result, selecting smaller decay rates yields faster transient responses and smaller stability margin. Using \( K \) and \( L \), determined in the first two steps, the remaining steps can be proceeded with \( \Delta L_{ge} = 0.01 \) mH to determine valid values of \( L_{ge} \) which is \( L_{ge} \in [0.2, 0.9] \) mH.

Fig. 4 shows the eigenvalue map of the closed-loop system in (32) with different values of \( L_{ge} \). It can be observed that the eigenvalues move toward the boundary of the unit circle as the grid inductance increases. In fact, the closed-loop system reaches the stability boundary at \( L_g = 105 \) mH. Nevertheless, the eigenvalues remain within the unit circle for the predefined values of \( L_{ge} \), which indicates the robustness of the proposed control scheme under parameter variations.

### C. Reference Tracking and Grid Synchronization

Considering the complete control input shown in Fig. 2, the inverter state-space equation can be extracted from (32) as

\[
x(k + 1) = A(x(k) + B(\alpha x(k) + BKx(k) - B\bar{r}(k) + B\bar{N}(k) + Bw_t(k)).
\]

It is clearly shown in (33) that the inverter states are independent of grid voltage in the steady state when the
estimated disturbance is equal to the real disturbance \( \bar{w}_{\text{fe}} = w_{\text{fe}} \). In fact, feeding back estimated disturbance can compensate not only for the grid voltage but also for the effect of parameter variation. That is, as shown in Fig. 5, the gain and phase of the closed-loop system at the specified frequencies are fixed for different values of \( L_g \). Oftentimes, the grid strength is conveniently indicated by short-circuit ratio (SCR) as

\[
SCR = \frac{1}{L_g \omega_g} \cdot \frac{q}{\sqrt{1+q^2}} \cdot \frac{E^2}{P} \tag{34}
\]

with \( q \) being \( X/R \) ratio of the grid. Since the gain and phase of the closed-loop system are independent of the grid voltage and inductance in the steady state, \( N \) can be chosen such that the closed-loop system has unity gain and zero phase delay at the fundamental frequency. For the inverter system given in Table I, the feedforward gain \( N \) is

\[
N = \frac{1}{0.1062} \angle (-7.0779^\circ) = 9.4162 \angle 7.0779^\circ. \tag{35}
\]

In addition to the grid voltage, the estimated disturbance contains also the compensation component for parameter variation. However, despite this compensation part, the angle of estimated disturbance is nearly the same with that of the grid voltage as will be shown in later sections. Therefore, the angle of estimated disturbance at fundamental frequency can be used to determine the grid phase angle. Since the estimator also acts as a band-pass filter, the estimated disturbance at fundamental frequency is free of harmonics. Therefore, only the conventional phase-locked loop [33] is sufficient to identify the grid voltage angle. The fundamental component of estimated disturbance is

\[
\bar{\varepsilon}_f = Wx \tag{36}
\]

where \( W = [0^{1 \times 4} \quad -1 \quad 0^{1 \times 7}] \).

In the steady state, when \( \bar{w}_{\text{fe}} = w_{\text{fe}} \), Equation (33) can be written as

\[
x(k+1) = A(\alpha)x(k) + BKx(k) + BNr(k). \tag{37}
\]

Equation (37) indicates that the closed-loop system is not affected by the grid voltage in the steady state. Furthermore, the influence of the variation in grid impedance is also taken into consideration as an input parameter in III.A and III.B. As a result, the proposed control scheme is capable of dealing with grid voltage disturbance as well as grid impedance variation.

IV. SIMULATION RESULTS

To validate the proposed control scheme, the simulations based on PSIM software have been carried out. The system parameters are described in TABLE I. Decay rates and parametric matrices of the estimator given in Section III are used to synthesize the proposed control scheme. In addition to the ideal grid voltage, unbalanced and distorted grid voltages are also employed to validate the performance of the proposed control scheme. This abnormal grid voltage has 20% reduction in magnitude in one phase and contains the 5th, the 7th, and the 11th harmonics with the magnitude of 10%, 10%, and 5% with respect to the nominal grid voltage, which yields the total harmonic distortion (THD) value of 15%.

To highlight the effectiveness of the proposed control scheme, the controller presented in [23] is adopted for the comparison purpose. Fig. 6 shows the simulation results under grid voltage dip of 25% for the control scheme presented in [23] and the proposed control scheme. It can be observed that in the event of a sudden reduction in magnitude of grid voltage, both control schemes exhibit similar short transience in injected current, taking about 10 ms to bring the inverter system back to the steady state.

Fig. 7 shows the comparison results under unbalanced and distorted grid voltages for the control scheme presented in [23] and the proposed control scheme. As can be seen in Fig. 7a, as soon as the voltage disturbance is superimposed on the ideal voltage at \( t = 0.2 \) s, the grid-side currents become unacceptably distorted. The distorted current brings about noticeable steady-state error in the estimated voltage, which is more clearly shown in the phase error between the grid phase angle and the estimated angle. On the contrary, as shown in Fig. 7b, the proposed controller can effectively remove the effect of grid voltage disturbance on the injected current after a transient period of 15 ms. Such a good disturbance rejection
The capability of the proposed control scheme is achieved by the incorporation of the disturbance model in the controller.

Fig. 8 compares the performance of the two control schemes considering a sudden change in grid impedance at $t = 0.12$ s. As can be observed from Fig. 8a, the controller in [23] is unable to stabilize the inverter system under such a condition. However, as shown in Fig. 8b, such variation in grid impedance unnoticeably affects the inverter operation when the proposed control scheme is used. Indeed, the inverter currents only encounter a relatively short transient period. This property of the proposed controller is achieved by using LMI approach to consider the grid impedance variation in the controller design.

In comparison to the existing controller, the proposed scheme provides better control performance in case of grid harmonic voltage and parameter variation. The shortcoming in dealing with harmonic disturbance in the existing controller comes from the fact that the proportional resonant (PR) controller and the estimator contain only the fundamental frequency term [23]. Therefore, the existing approach is not capable of rejecting harmonic voltage. Moreover, the grid inductance variation was not considered in [23]. Indeed, the robustness against parameter variation in [23] is just byproduct effect rather than a predefined design criterion. On the other hand, as clearly shown in Section III, both the harmonic voltage and grid induction variation are considered as design criteria in the proposed control scheme. Particularly, the parameter variation is considered in the proposed scheme by means of polytopic modeling and LMI-based design approach. Also, the grid voltage disturbance is modeled by using the IMP as shown from (19) to (25). As a result, the
The experimental results are shown in Fig. 9 and Fig. 10. The waveforms of grid voltage and grid-side current confirm the stable inverter operation during the start-up process.

To further validate the practicality of the proposed control scheme, the experiments using a three-phase 2.5 kVA prototype grid-connected inverter have been performed. The prototype grid-connected inverter is implemented using an intelligent power module (IPM) PM50RL1A120 with the turn-on and turn-off dead times of 1 μs within the DSP. The system parameters, the control gains, and the grid conditions are the same with those used in the simulations. The entire control scheme is implemented using 32-bit floating-point DSP TMS320F28335 with clock frequency of 150 MHz. The sampling and switching frequencies are chosen as 10 kHz. To emulate the grid, a three-phase programmable AC power source (PACIFIC Power Source 320-ASX) is connected to the filters through a transformer (PACIFIC M99211).

Fig. 9 shows experimental waveforms of the proposed control scheme during the start-up process which is one of the most critical tasks faced by voltage sensorless control schemes. As can be seen from Fig. 9, it takes about 30 ms for the estimated grid voltages to reach the actual values. Such transience can also be obtained from the estimated grid voltage angle. The waveforms of grid-side currents confirm a stable inverter operation during the start-up process.

Fig. 10 shows the experimental results of the proposed control scheme when the ideal grid voltages are suddenly changed into unbalanced and distorted voltages and Fig. 11 shows the experimental results when there is a step change in reference currents under unbalanced and distorted grid. All the experimental results are well matched with the simulation waveforms in Fig. 7. It is confirmed from these experimental results that the proposed voltage sensorless control scheme which uses only the grid-side current measurements operates stably as well as effectively even in such a severe grid environment.

As also seen in Fig. 9 and Fig. 10, the estimated grid voltages well assemble the actual grid voltages. However, there is small difference in magnitude between the actual and
the estimated voltage because of the imperfection of the experimental system. It should be also noted that such steady-state mismatch in estimated voltage does not affect the estimated grid angle and tracking performance of the proposed control scheme.

To investigate the quality of injected currents in an experimental way, Fig. 12 shows the experimental results of the proposed control scheme under unbalanced and distorted grid voltages. In spite of the abnormal grid voltages, the grid-side currents are considerably sinusoidal. In fact, according to the FFT result of $a$-phase current, the harmonic contents of injected currents well satisfy the limits specified by the grid interconnection regulation IEEE 1547 with the THD value of only 2.44%.

**VI. CONCLUSION**

This paper has presented a voltage sensorless control scheme for a three-phased grid-connected inverter. The proposed control scheme not only controls the inverter system stably under grid impedance variation but also ensures the high-quality injected current by using only the grid-side current sensors. Structurally, the proposed scheme mainly consists of a state feedback regulator and an extended-state estimator. Furthermore, the IMP and LMI approach have been employed to address different types of grid disturbance. Unlike the existing methods, this study proposes a systematic framework for designing a controller which is able to cope with grid voltage distortion as well as grid impedance variations at the same time. Comparative simulations under different operating conditions have shown significant improvements in control performance by using the proposed scheme. Moreover, the various experimental results based on a prototype grid-connected inverter have been presented to confirm the feasibility of the proposed control approach.

**REFERENCES**


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