Abstract—This paper presents a control scheme in the stationary reference frame for Static Synchronous Compensators that provides three features: 1) regulation of the positive sequence voltage to the nominal value, 2) full cancellation of the negative sequence voltage, and 3) peak limitation of the injected currents. These features can help improving the performance of these devices under perturbed grid conditions, including low, high and unbalanced voltages. Also, the peak current limiter guarantees both the safe operation of the converter, and a priority-based achievement of the voltage regulation targets, which is of interest during severe perturbations like voltage dips. Experimental results, implementation details and closed-loop analysis are presented to validate the regulation capabilities and to demonstrate the feasibility of the power quality improvement.

Index Terms—Reactive power control, grid fault, voltage dip, voltage sag, voltage support, positive and negative sequence control.

I. INTRODUCTION

POWER quality is a major concern for distribution and transmission system operators. Environmental issues and economical constraints have inspired new methods for controlling the grid-tied elements that are present in the electrical system [1], [2]. Among these new methods, there exists a noticeable interest in extending the use of power electronics converters to improve the power quality of the grid and to facilitate the integration of renewable and cleaner distributed generation sources [3], [4].

Static Synchronous Compensators (STATCOMs) are power electronics equipments that provide fast reactive power. They can be located close to distributed generation power plants or in selected places where the grid need to be precisely supported. The main objectives of these devices are to increase the controllability of the system, to reduce losses, to modify the power factor in selected locations, to regulate the voltage and to support the grid during voltage dips [5]. Moreover, the massive integration of power electronics equipments into the grid, has resulted in a new generation of ancillary services for distributed STATCOMs. Harmonic compensation, voltage imbalance mitigation, inertia emulation or damping oscillations, are some of the new functionalities that power plants shall attend in future grid codes [6]–[8].

In this context, an advanced control for a STATCOM is presented in this work. The proposed controller is focused on three main control objectives: 1) to regulate the positive sequence voltage to the nominal value, 2) to completely cancel the negative sequence voltage, and 3) to implement a peak current limiter with priority assignment that ensures a safe operation of the STATCOM in any circumstance. The control proposed for the STATCOM is developed in the stationary reference frame, and uses the symmetric sequences tool to achieve these objectives. In ideal grid conditions, the three-phase voltages should be balanced at the nominal value, which translates into a positive symmetric sequence equal to 1 per unit (p.u.), and a negative symmetric sequence equal to zero. These are the desired voltage objectives of the proposed control scheme.

The main challenge of the proposed control is the full cancellation of the negative sequence voltage, which is a common perturbation in electrical systems. The main cause of such a problem arises from the unbalanced loads across the distribution system. This perturbation causes power quality issues as for example pulsating torque in motor loads, which deteriorates its operation and life-cycle [9], [10], or higher power losses due to the overheating phenomenon [10], [11]. The full cancellation of the voltage imbalance with conventional control algorithms in the $\alpha\beta$ reference frame presents some problems. In particular, the numerator and denominator of the reference currents are built from a voltage that tends to vanish as the measured voltage tends to zero, thus causing the reference currents to become undetermined and loosing the synchronization in the injected currents. This problem will be highlighted in Section II-C, and the proposed solution to properly address this issue will be presented in Section III.

Full cancellation of negative sequence voltage has been briefly discussed in the literature [12]–[21]. Some of these works [12]–[15] cannot cancel the voltage imbalance completely, but until a certain limit due to the loss of synchronism previously mentioned. Other research works [16]–[18], present only simulation results, or provide few details regarding the implementation and the results obtained [19], [20]. Also, some interesting works are presented in [18], [20], [21] although these investigations are not based on sequence extractors, but on indirect power-based methods or in the machine model of
double fed induction generators. Finally, [22], [23] are focused on cancelling the negative sequence voltage in microgrids, although the controller is not suited to ride through voltage dips or swells, contrary to the proposed work.

Table I collects the main characteristics of state-of-the-art works to clearly highlight the differences of the proposal with the existing research. Several topics are analyzed in this table. In particular it has been compared the capability to regulate positive sequence voltage, negative sequence voltage and peak current limitation, which are the three main control objectives of the proposal. Also, the capability to ride through voltage dips or swells, the grid impedance knowledge and dependence, the implementation complexity and the application where the control is applied have been considered for the comparison. The table shows that for the given control objectives proposed in the work, the proposal presents interesting features for STATCOM applications. As a drawback of the proposed work, the control scheme requires a certain knowledge and it is dependent on the grid impedance, which is a common drawback when supporting the voltage during sudden perturbations as grid faults.

The proposed control scheme developed along this work uses a simple algorithm to fully cancel the negative sequence voltage by using a typical symmetric sequence controller. In fact, one of the main features of the proposal compared with previous works is its simplicity, since the typical structure of independent positive and negative sequence controllers is used, the tuning of the controller is straightforward, and the performance is fairly good with a low complexity. This objective is accomplished by using a virtual voltage estimator that allows the full cancellation of the voltage imbalance. Also, the proposal is developed in the stationary reference frame, which constitutes a new development in this research area, as opposed to most of the state-of-the-art proposals which are based on synchronous reference frame controllers.

The next part of the work is related to the third control objective, the safe operation of the STATCOM in front of sudden grid perturbations as for example voltage dips or swells. Under these severe perturbations, the converter could trip by over-current. To avoid this drawback, a peak current limiter is developed. The main characteristic of the peak current limiter is to avoid injecting more current in any phase than the maximum rated current of the power converter. Several peak current limiters have been proposed in the literature [24]–[29]. Some of them are used for the particular objective of cancelling dc-link oscillations [27]. The limiters in [28], [29] are quite complex and generalized for maximum admissible total active or reactive powers, requiring extensive computational resources. However, the proposed algorithm is implemented in two steps yielding to a priority-based development, which constitutes a new contribution in this area. The first priority of the limiter is to regulate the positive sequence voltage, and the second priority is to cancel the voltage imbalance. This methodology is aligned with typical grid operator needs. As a result, a low complexity computation of the maximum positive and negative reactive currents is obtained, ensuring that the injected phase currents will not surpass a given threshold.

To summarize the main control objectives developed along this work, the first one is the positive sequence voltage regulation, the second one is the full cancellation of the negative sequence voltage, and the third one is the peak current limiter. The second control objective constitutes the main contribution of the work, since it provides enhanced power quality characteristics to the STATCOM. The third objective is the second contribution of the work and helps to safely operate STATCOMs during any grid voltage perturbation. Finally, it should be highlighted that these three control objectives have not been previously combined in one single implementation and they provide advanced features for improving the power quality of the grid. Theoretical analysis and practical implementation issues are widely discussed, including the closed-loop analysis. Also, experimental results are provided to demonstrate the power quality improvement in a low-power scaled-down laboratory prototype.

The rest of the paper is organized as follows, Section II presents the plant and the control scheme of the STATCOM. Section III develops the proposed controller. Section IV presents the closed-loop analysis. Section V shows the experimental results. Finally, Section VI concludes the paper.

## II. Problem Statement

This section presents the basics for the plant and the control of the STATCOM. It introduces the main control objectives and the issues related to conventional controllers when cancelling the negative sequence voltage.
B. Control Objectives

The control objectives developed for the STATCOM can be formulated as

\[
\begin{align*}
V_{\text{ref}}^+ &= 1\ \text{p.u.} \quad (1) \\
V_{\text{ref}}^- &= 0\ \text{p.u.} \quad (2) \\
\max(I_{\alpha}, I_{\beta}, I_{c}) &\leq I_{\max}. \quad (3)
\end{align*}
\]

The first control objective in (1) is in charge of regulating the positive sequence voltage to 1 p.u. This means that whenever the positive sequence voltage surpasses the nominal value, lagging reactive power will be compensated by the STATCOM. Similarly, for low positive sequence voltage, leading reactive power will be injected to increase the voltage. It should be noticed that other voltage objectives can also be scheduled during daily peak or valley scenarios to improve the system performance, although 1 p.u. is selected for simplicity.

The second objective in (2) tries to remove the negative sequence voltage at the terminals of the STATCOM. Thus, cancelling the voltage imbalance and improving the power quality of the system.

The third objective in (3) implies that the maximum phase current provided by the STATCOM must be safely limited to the maximum rated current of the converter \(I_{\max}\). Then, it is guaranteed a safe operation that avoids the over-current tripping. This peak current limiter with priority assignation is devoted to extend the applicability of the control proposal not only to conventional operation modes, but also to balanced and unbalanced voltage swells and dips of any severity, independently of the stiffness/weakness of the grid.

C. Control Issues in Conventional Reference Generators

As stated above, the control proposal is developed in the stationary reference frame. Clarke's transformation [30] is used to translate the phase voltages and currents into the corresponding \(\alpha\beta\)-components.

Conventionally, a positive and negative sequence reference generator has been employed to inject positive and negative sequence reactive currents in the STATCOM. For doing this, a sequence extractor is required. For a detailed explanation of the sequence extractor, a literature review and the implementation details, see [31]–[33], and the references therein. Along this work, second order generalized integrators (SOGI) will be used as the sequence extractor [33]. Reference generators based on symmetric sequences consist of four terms, two related to the positive sequence component, and two for the negative one. Each one of these parts is also divided in two more pieces, one related to the amplitude of the current to be injected, and the other part related to the sinusoidal shape. A conventional current reference generator [25] used for this purpose is given in the following

\[
\begin{align*}
\dot{i}_{\alpha}^+ &= I_q^+ \frac{v_{\beta}^+}{\sqrt{3}} + I_q^- \frac{v_{\beta}^-}{\sqrt{3}} \quad (4) \\
\dot{i}_{\beta}^- &= -I_q^+ \frac{v_{\alpha}^+}{\sqrt{3}} - I_q^- \frac{v_{\alpha}^-}{\sqrt{3}} \quad (5)
\end{align*}
\]

where \(i_{\alpha}^+\) and \(i_{\beta}^-\) are the reference currents in the \(\alpha\beta\)-channels, \(I_q^+\) and \(I_q^-\) are the amplitude of the positive and negative reactive currents respectively and need to be computed based on the desired control objective, \(v_{\alpha}^+\), \(v_{\beta}^+\), \(v_{\alpha}^-\) and \(v_{\beta}^-\) are the instantaneous positive and negative voltages at the PCC after being processed by a sequence extractor, and \(V^+\) and \(V^-\) are the amplitude of the positive and negative sequence voltages computed as

\[
\begin{align*}
V^+ &= \sqrt{(v_{\alpha}^+)^2 + (v_{\beta}^+)^2} \quad (6) \\
V^- &= \sqrt{(v_{\alpha}^-)^2 + (v_{\beta}^-)^2}. \quad (7)
\end{align*}
\]

However, there is a problem with the above presented reference generator in (4) and (5). This reference generator is unable to fully cancel the negative sequence voltage because when the instantaneous negative sequence voltages \(v_{\alpha}^-\) and \(v_{\beta}^-\) tend to disappear, then their amplitude \(V^-\) also tends to zero, and the reference generator becomes undetermined. In such a case, both the amplitude and the shape of the negative sequence terms in (4) and (5) tend to zero, which causes the reference currents to lose the synchronization with the negative sequence voltages.

A simulation under these conditions, based on the reference generator in (4) and (5), is presented in Fig. 2(a). In this plot, the injected currents and the negative sequence voltage amplitude at the grid \(V_{\text{grid}}^-\) and the PCC side \(V^-\) are shown. It can be appreciated how the controller works as expected (i.e. the negative sequence voltage is being reduced) until reaching a very low voltage value. After this point, the reference current generator fails and the injected currents contain harmonics and cannot cancel the negative sequence voltage. This is a common problem of some controllers that use a measured magnitude that vanishes to build a reference signal. As a result, the reference should be zero. But in the case under discussion, the injected negative sequence reactive current cannot be null when trying to cancel the negative sequence voltage. Therefore, the conventional reference generator must be improved to fix this issue.

A new simulation with the control proposal developed in this work, fixing the issues shown in the previous simulation, is presented in Fig. 2(b). Note that the amplitude of the negative

Fig. 1: Plant model of the STATCOM.
sequence at the PCC tends to zero, and the injected currents lack of harmonics. The step-by-step realization of the proposal is given in next Section.

III. CONTROL PROPOSAL

The proposed control scheme to fix the above mentioned issue is based on the generation of a virtual voltage that does not vanish when the negative sequence voltage is cancelled, which is the basis for the generation of a reference voltage that can effectively cancel the negative sequence voltage as will be shown in the following.

A. Virtual Voltage

The virtual voltage needed for the proposed reference generator is built based on the dynamic model of the plant presented in Fig. 1.

\[
\hat{v}_\alpha = v_\alpha - \frac{L}{dt}i_\alpha
\]

\[
\hat{v}_\beta = v_\beta - \frac{L}{dt}i_\beta
\]

where \(\hat{v}_\alpha\) and \(\hat{v}_\beta\) are the virtual voltages in the stationary reference frame, \(v_\alpha, v_\beta, i_\alpha\) and \(i_\beta\) are the Clarke transformation of the measured voltages and currents respectively, and \(L\) is a virtual inductance, needed for the control scheme. Note that the virtual voltage exists only inside the controller, and emulates the voltage seen at any given distant point far from the converter. The purpose of the virtual voltage is to avoid loosing the synchronization both in the numerator and the denominator of (4) and (5) when approaching zero, resulting in the adverse effect shown in Fig. 2(a). The virtual voltages \(\hat{v}_\alpha\) and \(\hat{v}_\beta\) obtained from (8) and (9) have some interesting features that make the control proposal capable to fully cancel the negative sequence voltage. The main characteristic is that the virtual voltages are in phase with the original measurements, due to the fact that the STATCOM only injects reactive current. However, the amplitudes of virtual and measured values are slightly different, depending on the virtual inductance and the injected currents. Moreover, even when the measured voltages are zero, the virtual voltages used in the reference generator keep the appropriate phase to inject the desired reactive power, since they are reproducing a distant point where the amplitude is non-zero. Therefore, these characteristics avoid the loss of synchronization in the reference currents and allows the STATCOM to fully cancel the negative sequence voltage.

In fact, the virtual voltage estimator provides enhanced characteristics to the closed-loop system as will be shown in Section IV. Also, note that the time derivative used in the virtual voltage computation does not have adverse effects within the control loop (this term normally introduces high-frequency noise in the control algorithm) due to the fact that it is post-processed by a sequence extractor, as will be shown below.

B. Sequence Extractor

It should be noticed that the virtual voltage must be filtered to remove noise and harmonics. The symmetric sequence extractor performs this filtering and also computes the positive and negative instantaneous virtual voltages in the stationary reference frame \(\hat{v}_\alpha^+, \hat{v}_\beta^+, \hat{v}_\alpha^- \hat{v}_\beta^-\).

Similarly as in (6) and (7), the amplitudes of the positive and negative sequence virtual voltages are obtained as

\[\hat{V}^+ = \sqrt{(\hat{v}_\alpha^+)^2 + (\hat{v}_\beta^+)^2}\]

\[\hat{V}^- = \sqrt{(\hat{v}_\alpha^-)^2 + (\hat{v}_\beta^-)^2}\]

In addition, the angle between the positive and the negative sequence virtual voltages \(\hat{\phi}\) can be obtained as

\[\cos\hat{\phi} = \frac{\hat{v}_\alpha^+\hat{v}_\beta^- - \hat{v}_\beta^+\hat{v}_\alpha^-}{\hat{V}^+\hat{V}^-}\]

\[\sin\hat{\phi} = \frac{\hat{v}_\alpha^+\hat{v}_\beta^+ + \hat{v}_\alpha^-\hat{v}_\beta^-}{\hat{V}^+\hat{V}^-}\]

\[\hat{\phi} = \text{atan2}(\sin\hat{\phi}, \cos\hat{\phi})\]

where \(\text{atan2}\) is the two-argument arctangent function. The angle between both symmetric sequences, \(\hat{\phi}\), will be used to compute the peak current limiter.

C. Reference Generator

The reference generator that is used to generate the reference currents is

\[i_\alpha^* = I_q^+\frac{\hat{v}_\beta^+}{V^+} + I_q^-\frac{\hat{v}_\beta^-}{V^-}\]

\[i_\beta^* = -I_q^+\frac{\hat{v}_\alpha^+}{V^+} - I_q^-\frac{\hat{v}_\alpha^-}{V^-}\]

The only difference between (15) and (16) and the conventional reference generator in (4) and (5), is the use of a virtual voltage \(\hat{v}\) instead of the voltage at the terminals of the converter \(v\). In the conventional case, the sequence voltages...
D. Reactive Current Controller

The next step in the control proposal is the calculation of the amplitude of the reactive currents $I_a^+$ and $I_b^+$ in (15) and (16). As previously mentioned, these magnitudes depend on the control objectives. For the proposed objectives in (1) and (2), the current references are derived from the voltage support analysis as

$$I_a^+ = \frac{V_{ref}^+ - \hat{V}_a^+}{\omega L} \tag{17}$$

$$I_b^+ = -\frac{V_{ref}^+ - \hat{V}_b^+}{\omega L} \tag{18}$$

where it is shown that the positive sequence current $I_a^+$ depends on the target voltage $V_{ref}^+ = 1$ p.u., the virtual voltage $\hat{V}_a^+$, the grid frequency $\omega$, and the virtual inductor $L$. Similarly, the negative sequence reactive current $I_c^-$ depends on the objective voltage $V_{ref}^- = 0$ p.u. and the virtual computation $\hat{V}_c^-$, the grid frequency $\omega$, and the virtual reactor $L$. Note that (17) and (18) form a proportional controller of the symmetric voltage errors. The gain of these controllers is fixed and depends on the grid frequency and virtual inductance. A discussion on the selection of the virtual inductance $L_v$ and the closed-loop dynamics of the whole system are presented in Section IV.

The calculation of the reactive currents in (17) and (18), along with the virtual voltage artifact constitute the main contribution of the work since it is now possible to fully cancel the negative sequence at the terminals of the STATCOM by using a simple control based on the stationary reference frame.

E. Peak Current Limiter

Once the reference current generator, the virtual voltage estimator and the amplitude of the reference currents have been derived, the last remaining step is to provide the STATCOM with a peak current limiter. This limiter must ensure that the phase currents $I_a$, $I_b$ and $I_c$ are below the rated current $I_{max}$ of the converter. Also, as previously mentioned, the peak current limiter has a priority-based implementation which is of interest for the operation of the STATCOM whenever there is no enough current capacity to achieve the desired objectives as for example severe voltage perturbations or stiff grids which is the main limitation for voltage regulation when using STATCOMs.

Similarly to [28], [34], the amplitude of each phase current can be expressed as

$$I_a = \sqrt{(I_a^+)^2 + (I_a^-)^2 + 2I_a^+I_a^-\cos(\varphi_1)} \tag{19}$$

$$I_b = \sqrt{(I_b^+)^2 + (I_b^-)^2 + 2I_b^+I_b^-\cos(\varphi_1 + 2\pi/3)} \tag{20}$$

$$I_c = \sqrt{(I_c^+)^2 + (I_c^-)^2 + 2I_c^+I_c^-\cos(\varphi_1 - 2\pi/3)} \tag{21}$$

where $\varphi_1 = \pi - \hat{\varphi}$ is the angle between the positive and the negative sequence currents.

To develop the limiter, assume that the voltage at the terminals of the STATCOM is quite low, and the positive sequence reactive current $I_a^+$ that should be injected is very large. Under such condition, the priority-based behavior of the device should inject only positive sequence current and set the negative sequence reactive current as $I_c^- = 0$ A. Solving (19)-(21) for the positive reactive current, and taking into consideration that the injected current should be limited to $I_{max}$, then the first part of the current limiter is obtained

$$I_a^+ \leq I_{max}. \tag{22}$$

Now assume that the voltage perturbation is less severe, and the first objective in (1) is accomplished. Then, the STATCOM can inject negative sequence current $I_c^-$ to achieve the second objective in (2). Under these conditions, the peak current limiter becomes more complex since the combination of positive and negative sequence currents inherently induces a different amplitude in the injected currents $I_a \neq I_b \neq I_c$ [28]. This characteristic results in the need for computing the maximum negative sequence current $I_c^-$ in the three phases, and then select the minimum value of the three computations. By doing this, the STATCOM will operate safely and will exploit its capabilities to its maximum for any given grid perturbation.

Solving (19)-(21) for the negative sequence reactive current, the following relations are obtained

$$I_{q_a,\text{max}} = \sqrt{I_{max}^2 - (I_a^+)^2\sin^2(\varphi)} - I_a^+\cos(\pi - \hat{\varphi}) \tag{23}$$

$$I_{q_b,\text{max}} = \sqrt{I_{max}^2 - (I_a^+)^2\sin^2(\varphi + 2\pi/3) - I_b^+\cos(\pi - \hat{\varphi} + 2\pi/3)} \tag{24}$$

$$I_{q_c,\text{max}} = \sqrt{I_{max}^2 - (I_a^+)^2\sin^2(\varphi - 2\pi/3) - I_c^+\cos(\pi - \hat{\varphi} - 2\pi/3)} \tag{25}$$

$$I_{q_{\text{max}}} = \min \left\{ I_{q_a,\text{max}}, I_{q_b,\text{max}}, I_{q_c,\text{max}} \right\} \tag{26}$$

where $I_{q_{a,\text{max}}}$, $I_{q_{b,\text{max}}}$ and $I_{q_{c,\text{max}}}$ are the maximum negative sequence reactive currents available in each phase to avoid surpassing $I_{max}$, and $I_{q_{\text{max}}}$ takes the minimum of the three computations.

F. Implementation

In order to further detail the implementation, the control block diagram of the proposed controller is presented in Fig. 3. From this plot, it should be highlighted the block related to the virtual voltage estimator, that captures the measured
voltages and currents to build the virtual values from (8) and (9). The virtual values will be used in the positive and negative sequence controllers after being processed by the sequence extractor. Two symmetric sequence controllers are used. As previously mentioned, each one of these controllers consists of a proportional term of the voltage error, see (17) and (18). The peak current (22)-(26) and the reference generator (15)-(16) are the last part of the voltage loop. The outputs go directly to the inner current loop of the STATCOM. The current loop joins a proportional-resonant (PRES) controller and a space vector modulator to obtain the control signals to drive the switches of the converter.

Finally, the pseudo-code of the proposal is presented in Fig. 4. The controller algorithm is divided into several parts: sampling and processing, virtual voltage estimator, reactive current calculation, peak current limiter, reference generator and current loop. This pseudo-code presents a complete implementation of the proposal, showing detailed characteristics of the computations required for the whole controller. Note for example that in line number 16 of Fig. 4, the reactive current $I_q^+$ could be negative during voltage swells and need to be checked to ensure the objective presented in (3). Also, it is interesting to note that in lines 24 to 26, the negative sequence current limit is to avoid over-current tripping, and to implement the priority assignment previously described, which prioritizes the positive sequence regulation in front of the negative sequence voltage support.

### IV. Closed-Loop Analysis

After presenting the main theoretical findings of the paper and the implementation details, one point still remains to be analyzed. This section covers the closed-loop dynamics of the proposed control scheme and concludes with the selection of the virtual inductance $\hat{L}$ in a real application.

The closed-loop dynamics has been obtained based on the following assumptions: i) other dynamics of the STATCOM are decoupled from the presented closed-loop analysis, and ii) the plant dynamics regarding the voltage support are neglected. The first assumption states that the STATCOM can provide the required reactive power without being affected by the dc-link voltage controller whose dynamics should be adjusted to allow sudden reactive current variations. The second assumption simplifies the analysis by considering that the changes in the reactive currents injected have a fast dynamics that cause the voltage variations almost instantaneously. Therefore, the voltage support equations can be written as

$$V^+ = V_g^+ + \omega LI_q^+$$
$$V^- = V_g^- - \omega LI_q^-$$

where it can be proved that the injected currents support the voltage (an increment in the positive sequence, and a reduction in the negative one) proportionally to the injected current, the grid frequency and the grid impedance. This is a common assumption in voltage support control analysis.

Based on the control scheme presented in Fig. 3, the transfer functions needed to analyze the closed-loop system are presented in Fig. 5. The positive sequence channel is presented in Fig. 5(a), and the negative sequence blocks are collected in Fig. 5(b). From this figure, the main transfer functions in the Laplace domain are detailed as follows.

The controller transfer function consists on a proportional gain according to (17) and (18), therefore the positive and the negative transfer functions are

$$G_{\text{control}}^+(s) = \frac{I_g^+(s)}{e^+(s)} = \frac{1}{\omega L} \quad , \quad G_{\text{control}}^-(s) = \frac{I_g^-(s)}{e^-(s)} = -\frac{1}{\omega L}$$

The dynamic behavior of the SOGI is complex. However its linearized formulation [35] allows to simplify the dynamics in the ratio of the virtual current to the injected current.
both for the positive and negative sequences as a low-pass filter with cutoff frequency equal to $\xi\omega$ as

$$G_{\text{logi}}(s) = \frac{\hat{V}^+(s)}{V^+(s)} = \frac{\hat{V}^-(s)}{V^-(s)} = \frac{\xi\omega}{s + \xi\omega} \quad (30)$$

where $\xi$ is the selectivity of the filter [33].

A simplified characterization of the the current loop is obtained by considering it as a first order system. This model considers that the current reference from the outer voltage loop takes some time to effectively being injected by the inner current loop into the grid. The positive and negative transfer functions are symmetric and can be represented as

$$G_{\text{current}}(s) = \frac{I^+_q(s)}{I^+_q(s)} = \frac{I^-_q(s)}{I^-_q(s)} = \frac{1}{\tau s + 1} \quad (31)$$

The last part corresponds to the voltage support provided by the STATCOM at the PCC with respect to the grid. The magnitude of this voltage variation has been presented in (27) and (28) where $V^\pm_g$ are the grid voltages considered as perturbations within the proposed analysis. Based on these relations, the plant is considered as a pure gain

$$G_{\text{plant}}^+(s) = \frac{\Delta V^+(s)}{I^+_q(s)} = \omega L \quad , \quad G_{\text{plant}}^-(s) = \frac{\Delta V^-(s)}{I^-_q(s)} = -\omega L \quad (32)$$

Finally, the closed-loop behavior of the virtual estimator is similar to any state estimator whose inputs are the measured plant outputs and the applied control signal, while the output is the estimation value. The following relations are performed inside these blocks

$$\hat{V}^+(s) = V^+(s) - \omega \hat{L} I^+_q(s) \quad (33)$$

$$\hat{V}^-(s) = V^-(s) + \omega \hat{L} I^-_q(s) \quad (34)$$

Joining all these parts together, it is possible to obtain the output dynamics as

$$V^+(s) = G_{\text{closed-loop}} V^+_\text{ref}(s) + G_{\text{perturbation}} V^+_g(s) \quad (35)$$

$$V^-(s) = G_{\text{closed-loop}} V^-_\text{ref}(s) + G_{\text{perturbation}} V^-_g(s) \quad (36)$$

where

$$G_{\text{closed-loop}} = \frac{L_s + L \xi \omega}{L \tau s^2 + (1 + \tau \xi \omega) L_s + L \xi \omega} \quad (37)$$

$$G_{\text{perturbation}} = \frac{L \tau s^2 + (1 + \tau \xi \omega) L_s}{L \tau s^2 + (1 + \tau \xi \omega) L_s + L \xi \omega} \quad (38)$$

The transfer function in (37) shows the dynamics of the proposed control scheme based on the plant parameters $\omega$ and $L$, the inner current loop characteristics $\tau$, the sequence extractor selectivity $\xi$ and the control parameter $\hat{L}$. This transfer function has been plotted in Fig. 6 to further detail the main closed-loop characteristics. The plot shows the closed-loop bode diagram for different values of the control parameter $\hat{L}$.

It is clearly shown, both from the analysis of the transfer function in (37), and in the magnitude response of Fig. 6, that the steady-state value when $\lim_{s \to 0} G_{\text{closed-loop}}$ has unity gain, as expected. Therefore, the steady-state does not depend on the virtual inductance which clearly demonstrates the enhanced properties of the proposed closed-loop control. From the closed-loop transfer function, it is interesting to note that the dominant pole, assuming that the inner current loop is fast enough, is located at $s = -\frac{\xi \omega}{L}$ which makes the system stable provided that $\hat{L} > 0$. As shown in Fig. 6, this dominant pole becomes a complex conjugate for low values of $\hat{L}$. From (37), this behaviour is reached whenever $\hat{L} < \frac{\xi \omega \tau s}{(1 + \xi \omega)}$.

A. Selection of the control parameter $\hat{L}$

In the following, several considerations are presented for the selection of the virtual inductance, including real impedance knowledge, closed-loop dynamics, stiffness/weakness of the grid, and rated voltage and current.

As a first approach, selecting $\hat{L} = L$ seems to be the best option. However, the real grid impedance can not be perfectly known due to daily variations or even the fault location. However, an approximate knowledge, although inaccurate, is always available when commissioning the STATCOM, and this
value will serve as the starting point for the design of the control parameter.

Due to the uncertainty in the grid impedance, it is required to analyze the effects on the closed-loop dynamics. Assuming that $\hat{L}$ has been selected based on a bad estimation of the real grid impedance, the transient dynamics is affected as follows. In case that the virtual value is larger than the real impedance, the dynamics is slow-down. Similarly, for smaller virtual than real inductance, the dynamics is speed-up. This effect can be appreciated in Fig. 6 for $\hat{L} = L + 100\%$ and $\hat{L} = L - 100\%$ respectively where the dominant pole move towards lower frequencies as $\hat{L}$ increases. Based on this information, it can be concluded that $\frac{\hat{L}}{L} > 1$ is of interest to reduce faster dynamics and avoid overshoots which are not the preferred option when supporting the voltage. However, this value should not be very large in order to provide an admissible settling time $t_s \approx \frac{4}{12\pi}$ during the perturbation.

Regarding the stiffness/weakness of the grid, the selection of the virtual inductance has no noticeable impact. Apart from requiring more or less current to achieve the voltage objectives, the controller performs as expected in any of these conditions. This property should be highlighted since it is possible to adapt the proposed controller to different network requirements.

Similarly, the rated voltage barely affects the proposed control. However, the rated capacity of the STATCOM must be taken into account. The reason for this is that typically, the higher the rated current, the lower the speed response of the current loop which is considered within the parameter $\tau$ in (31), and it is modelled in the transfer function (37). Therefore, to fastly react to grid perturbations as voltage dips, the current loop and the desired closed-loop dynamics should be fast enough. Otherwise, the STATCOM would not be capable to counteract these perturbations.

The aforementioned analysis of the static and dynamic behavior of the proposed control scheme yields to a simplified design guideline for the control parameter $\hat{L}$. The control design starts by identifying an approximate value of the real grid impedance $L$. Since this value can not be perfectly known, a trade-off between settling time and desired dynamics is required taking into account the uncertainty introduced by the plant parameter. By choosing $\frac{\hat{L}}{L} > 1$, the system slows down and the response time increases. By choosing $\frac{\hat{L}}{L} \approx 1$, the response time is reduced and the system is fast enough to counteract voltage dips and any other perturbation. The selection of $\frac{\hat{L}}{L} < 1$ is not recommended in any case although it does not compromise the stability. This guideline is simple and easy to implement, which is an important characteristic of the proposed control scheme that makes the proposal a good candidate to improve the power quality of the grid.

V. EXPERIMENTAL RESULTS

The control proposal has been tested in a laboratory prototype. The test-bench mimics the scheme shown in Fig. 1. It is made up of an Amrel DC power source to emulate the dc-link, a Guasch three-phase voltage source converter limited to $I_{\text{max}} = 10$ A, and a Pacific Power AC grid emulator. The grid impedance has been selected so as to emulate a weak grid connection ($L = 0.12$ p.u.). This scenario has been selected as the worst case for a STATCOM control, since these devices can be attached to remote locations where the grid is weak and the voltage regulation is a must. Even though, it should be noted that the performance of the proposed controller does not depend of the stiffness or weakness of the grid, since the peak current limiter and the priority assigned to each one of the voltage targets, makes the proposal independent of the grid impedance. Regarding the virtual inductance, and according to the analysis previously presented, it has been selected as $\hat{L} = 1.5 L$. This value has been selected according to the previous design criterion and to demonstrate that even for a mismatch of 50% in the knowledge of the grid impedance value, the proposed controller performs well. The main system and control parameters are collected in Table II.

The controller is implemented on a Texas Instruments F28M36 digital signal processor, according to the pseudo-code in Fig. 4. The sequence extractor and the resonant controllers for the current loop are based on SOGIs. The current loop also includes a space vector modulator to compute the switching times of each converter branch.

In order to demonstrate the performance of the controller in any type of grid perturbation, a complex test has been developed. The experiment covers the most diverse conditions for the device under test, including some imbalance, a voltage dip, the dip recovery and the connection of an unbalanced load. Phase voltages and currents are collected with a Yokogawa DL850 oscilloscope and exported to Matlab from representation, while the rest of the signals are directly obtained from the microcontroller. The test has been divided into 5 regions, each one lasting 0.1 seconds. The voltage measured at the PCC when the STATCOM is unplugged can be seen in Fig. 7.

In the top plot of Fig. 7, it is shown a small voltage imbalance programmed from $t = 0.1$ s to $t = 0.2$ s. In this region, the controller should cancel completely the negative sequence voltage.

The second part of the test, between $t = 0.2$ s and $t = 0.3$ s, corresponds to a voltage dip. In this case, positive sequence current injection is required to support as much as possible.

<table>
<thead>
<tr>
<th>TABLE II: System and Control Parameters</th>
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<tbody>
<tr>
<td>Symbol</td>
</tr>
<tr>
<td>base power</td>
</tr>
<tr>
<td>rated current</td>
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<tr>
<td>grid voltage</td>
</tr>
<tr>
<td>grid frequency</td>
</tr>
<tr>
<td>dc-link voltage</td>
</tr>
<tr>
<td>grid inductance</td>
</tr>
<tr>
<td>converter inductance</td>
</tr>
<tr>
<td>nominal load</td>
</tr>
<tr>
<td>switching frequency</td>
</tr>
<tr>
<td>sampling period</td>
</tr>
<tr>
<td>pos. seq. voltage ref.</td>
</tr>
<tr>
<td>neg. seq. voltage ref.</td>
</tr>
<tr>
<td>virtual inductance</td>
</tr>
<tr>
<td>SOGI selectivity</td>
</tr>
<tr>
<td>PRES prop. gain</td>
</tr>
<tr>
<td>PRES resonant gain</td>
</tr>
</tbody>
</table>
Fig. 7: Experimental measurements for the test scenario with the STATCOM unplugged. From top to bottom: phase voltages, rms voltages and test condition.

Fig. 8: Experimental results for the proposed control. From top to bottom: phase voltages, phase currents, rms voltages, positive sequence voltage, negative sequence voltage and reactive currents.

The positive sequence voltage, while supporting the negative sequence voltage is a non-priority objective. In this part of the test, the peak current limiter should activate and inject the maximum rated current of the STATCOM for voltage support.

The third zone occurs between $t = 0.3$ s and $t = 0.4$ s. During this interval, the voltage dip has been recovered and a typical over-voltage phenomenon is observed. The controller in such a situation should reduce the high positive sequence voltage, and the rest of the current will be used to reduce the unbalanced voltage, limiting the injected current in case that the STATCOM has not enough capacity.

The last part of the experiment, between $t = 0.4$ s and $t = 0.5$ s, explores the controller capabilities when an unbalanced local load is connected at the PCC. In this case, the voltage imbalance increases slightly and the controller needs to fully cancel this imbalance. The load changes from $R_a = R_b = R_c = 22\Omega$ to $R_a = R_c = 11\Omega$ and $R_b = 22\Omega$.

Fig. 8 shows the experimental results obtained during the experiment when the STATCOM is activated. The plot is divided into six parts showing the most significant magnitudes.

The top plot in Fig. 8 shows the instantaneous phase voltages at the PCC. Comparing the voltages in the previous figure (unplugged STATCOM) and the results in this figure (active STATCOM), it is shown the voltage regulating capabilities. Along the experiment the voltages tend to be closer to the nominal value. During the dip and the recovery ($0.2 < t \leq 0.4$ s), the voltages are far from the target objectives. However, a noticeable voltage support is observed due to the weak grid conditions.

The next plot in Fig. 8 presents the currents injected by
the STATCOM. In the second part of the experiment (0.1 < t ≤ 0.2 s), the currents are unbalanced to simultaneously comply with the voltage objectives. After that, during the voltage dip, the peak current limiter is activated, and the maximum rated current of the converter is injected. Since the first positive sequence voltage objective has higher priority, only positive sequence reactive current is required, and the currents are perfectly balanced and limited to I_{max}. Next step (0.3 < t ≤ 0.4 s) corresponds to the post-fault recovery, and typically an over-voltage situation can be manifested. Due to the high voltage, there is no enough current to fully cancel the negative sequence voltage, and the peak current limiter is activated again. Note that the current in phase A is safely limited to 10 A. Finally, at (0.4 < t ≤ 0.5 s), the fault has been perfectly cleared and an unbalanced load is connected at the PCC, causing a greater imbalance. In such a situation, the STATCOM is capable to regulate the positive sequence voltage to the nominal value, and to completely cancel the negative sequence voltage.

The third plot presents the rms magnitudes of the phase voltages. It is clearly shown the almost ideal voltage regulation capabilities of the controller, except in the region where the peak current limiter is activated because the converter has no enough capacity to achieve the voltage objectives.

The fourth and fifth plots present the positive and the negative sequence voltages. Also, it has been included the magnitudes when the STATCOM is deactivated in thin lines for comparison. The most important part of this graph is the full cancellation of the negative sequence voltage, which confirms the expected power quality improvement with the proposed control method.

Finally, the reactive currents injected are presented in the last plot. This plot shows the evolution during the different experiment parts, and the limitation when the peak current is activated. Also, in grey lines, the maximum rated current of the converter and the maximum allowable negative sequence currents are plotted for comparison. As an interesting detail, note that the current I_{q} will be negative during the recovery part of the experiment. Also note that when I_{q} is lower than I_{q,max}, the peak current limitation is not activated and the negative sequence voltage can be completely eliminated. Conversely, when I_{q} coincides with I_{q,max} (during the voltage dip and the recovery intervals), the peak current limitation is activated in order to safely operate the STATCOM.

VI. CONCLUSIONS

This paper has presented a STATCOM control with three main objectives: positive sequence voltage regulation, full negative sequence cancellation, and peak current limitation with priorities. The main contribution of the presented work is the inclusion of these three control objectives simultaneously in a low complexity, easy to tune implementation.

The solution proposed can help to improve the regulation capabilities of STATCOMs, being the full cancellation of the voltage imbalance an interesting ancillary service for a new generation of STATCOMs. The control is suitable for the operation during voltage dips or swells, and stiff or weak grid connections due to the priority-based peak current limiter. The main drawback of the proposed control scheme is the approximate knowledge of the grid impedance which makes the proposal dependent of this value, although it has been shown that the system presents good transient dynamics and steady-state behaviour even during not perfectly known grid impedance values.

Future work will focus on the integration of the proposed controller into complex systems, and the interaction with distributed generation sources implementing low voltage ride through requirements. Also, the coordination with planning and dispatching schedulers should be taken into account for the proper operation and optimization of the whole system.

REFERENCES


