

Intelligent-Field Transmitter (ITF) Based on Microcontroller

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Abstract—In this article the design of a prototype of an Intelligent Transmitter of Field (ITF) is presented. The objective of the work is approaching the engineering students, in the area of Automatic Control, to the functional and technological challenges intrinsic to this type of implementations. Aspects like power consumption of the electronic circuits, energy supply through the wires of communication of the transmitter, the coexistence in these wires of analogical signals of measurement and digital signals of communication, etc., are essential to get the foreseen objective. The article seeks, also, to contribute to a didactic solution for a problem that has been one of the main challenges in instrumentation field.

Keyword—Microcontroller-Based Systems; Industrial Communications; Digital Communications.

I. INTRODUCTION

Certain applications demand the use of transmitters whose energy supply is carried out through the own line for which they send the signal measured in the receiver. In places of difficult access it is convenient, also, that the confirmations and periodic calibrations of the transmitter are carried out in a comfortable way [1]. This will be possible if the transmitter is endowed with the capacity of 'to dialogue' with the operator without altering the measure signal that is transmitting. The design and development of any new transmitter takes implicit the necessity to be compatible with the analogical conventional transmitters and, also, to enlarge its operative benefits. Therefore, a habitual demand is the ability to substitute the conventional transmitters without the necessity of a new wiring or to alter the existent ones [1].

In this work the study, design and implementation of an Intelligent Transmitter of Field, (ITF), is presented. The ITF has to be able to take the supply energy and to establish the digital communication with the receiver only through two wires. At the same time, it is required to maintain the compatibility with the analogical traditional

transmitters of 4...20 mA. This aspect will allow that [2], besides carrying out the classical functions of its predecessors, these transmitters can be part as active elements in a more complex distributed control system (Fig. 1).

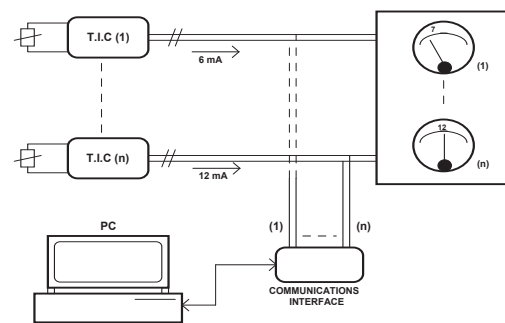


Fig. 1. Distributed data acquisition system.

II. CONCEPTION ENGINEERING

The above features for an ITF define certain characteristics that the transmitter should include in its design phase. From the functional point of view, it required that:

- The measure signal, in current form in the range from 4 to 20 mA, should be exclusively function of the physical variable measured by the sensor. This signal will not be altered by the intensity consumed by the electronics of the transmitter.
- In normal operation (transmitter connected to the receiver and the whole operative group), the signal of digital communication should have the characteristic of not perturbing the measure signal sent by the transmitter. In this way, the transmitter will be provided with functions such as: calibration, periodic confirmation, modification of the measure range, etc.

These previous requirements lead to solve the following technical aspects:

- a) Reduced consumption of the transmitter electronics for not influencing significantly in the magnitude of the measure current.
- b) Communication technique that presents the previously mentioned characteristics and that it doesn't alter the balance of present currents in the transmitter.

To give an answer to the outlined questions, we present the alternative design shown in Fig. 2. In this figure we observe the inclusion of a current generator governed by voltage (G1) whose intensity in the range from 2 to 18 mA will be exclusively function of the signal provided by the sensor. If we supply energy to the whole electronics by means of another generator (G2) with constant current of 2 mA, we will have in the communication line an analogical signal with a current inside the range of 4...20 mA whose variation will only be function of the physical magnitude measurement. The internal electronics would be made up basically by the input bridge, a PGDA (Programmable Gain Differential Amplifier), the converter A/D, D/A, and the microcontroller, μC . The communication signal, of digital nature, would be coupled to the line through capacitor C_1 .

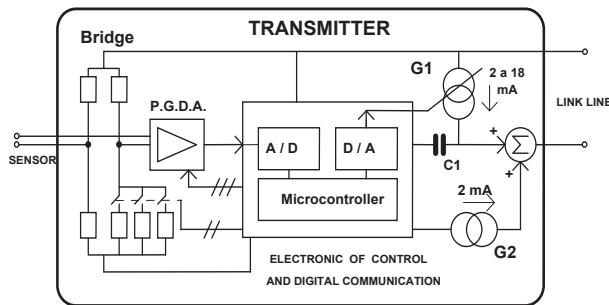


Fig. 2. Block Diagram of the ITF.

A. Input Bridge

The conditioning electronics for the signal coming from the sensor is implemented by a Wheatstone bridge allowing inputs coming from thermocouples, signals in current of 4...20 mA, resistances-temperature Pt-100 Ω or voltages in the range of 0..100 mV and 0..1 V. This electronic circuit will be protected with input varistors and with a circuit against the break of the thermocouple.

B. Amplifier Circuit

The amplifier will be made up by a PGDA (INA118). Depending on the chosen sensor, the amplifier will generate the corresponding output signal with the appropriate gain. This component presents a reduced consumption, a great linearity and a low thermal bias [3].

C. Analog-to-Digital Conversion Circuit

This block contains the 12-bit LTC1288 converter (Fig. 3). It has two input channels: The first is used as main channel of measurement, and the second is useful for sensing the environment temperature of the transmitter (this is necessary to compensate thermocouple signals), and for sensing the precision voltage reference (useful in the compensation of temporary drift).

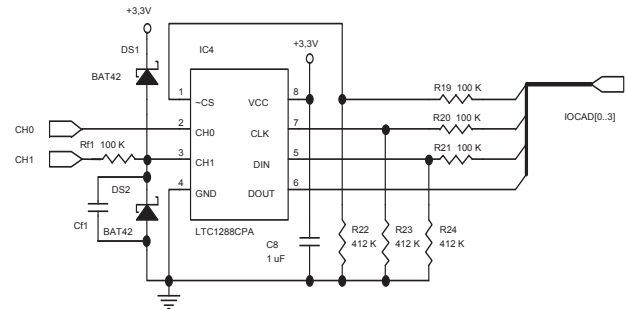


Fig. 3. Analog-to-digital conversion circuit.

D. Microcontroller Circuit

Based on a simple μC , the PIC16C65 model [5], this circuit operates at a low clock frequency to reduce power consumption. The μC will acquire the measurement, it will perform a linearization and finally it will control the DAC. Furthermore, the microcontroller will be responsible for interpreting messages received via the line that connects the receiver (Fig. 4).

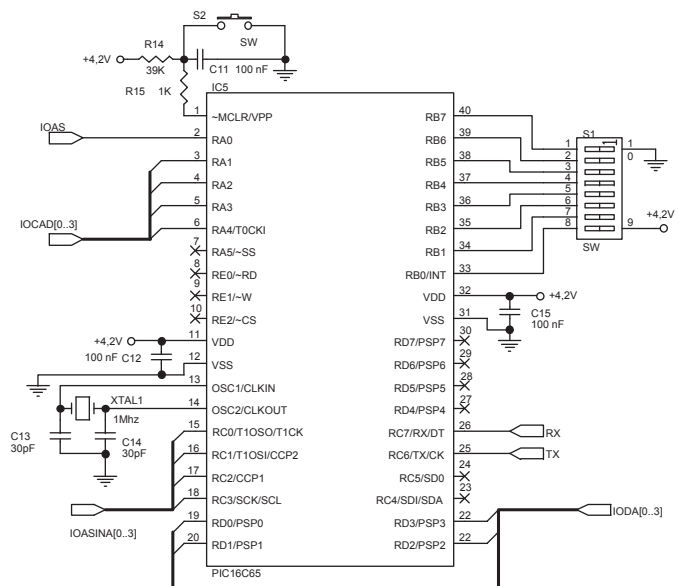


Fig. 4. Microcontroller circuit.

E. Digital-to-Analog Conversion Circuit (DAC)

This block includes the digital-analogical converter MAX 538 [4], serial type, with 12-bit resolution. It presents excellent characteristics of consumption (Fig. 5).

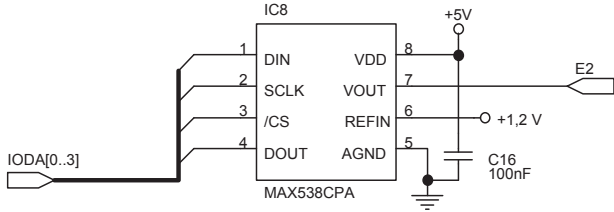


Fig. 5. Digital-analogical conversion circuit.

F. Analog Output Circuit

This circuit has the task of collecting the analogical signal provided by the DAC and 'adjust' it within the range of 4...20 mA. The current flows through the line that connects the transmitter and measurement equipment. The most important element of this stage is the XTR-101 circuit. This is a device that changes its transconductance depending on the voltage signal applied to its input. It has an auxiliary power supply, 2 mA, which will create the whole power system. This value of current (2mA) is the maximum allowed value for the consumption of all the electronic circuits that the transmitter incorporates (Fig. 6).

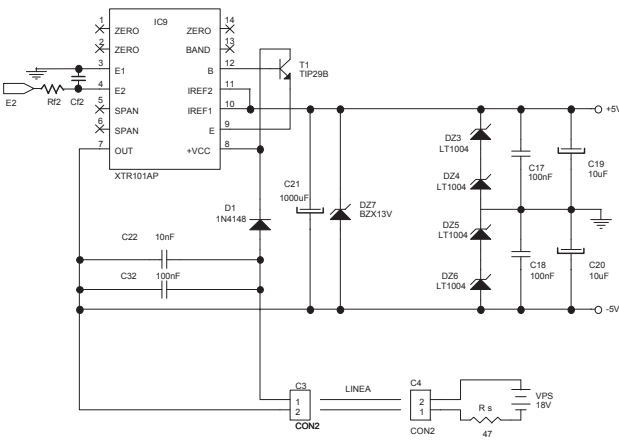


Fig. 6. Analogical output circuit.

G. Digital Communication System

An example of this type of communication in the transmission part is used in the instrumentation that uses HART protocol. As one of the objectives pursued in the design is teaching, we design a particular method of communication which modifies the intensity in the line of communication without changing its mean value (Fig. 7).

The digital communication will consist of two almost identical blocks, one located in the transmitter and the other located at the opposite end of the link line, i.e. close to the measuring equipment [6]. The latter constitutes a communication interface of a personal computer acting as the console.

The circuit of figure 7 is divided into three distinct parts:

- On the left there is the module located in the transmitter and it is in turn divided into three blocks: The operational amplifier (block reception), the capacitor C_1 and R_8 (block transmission) and the generator I_1 flow that simulates the output of the XTR101.
- In the central part there is the module that simulates the impedance of the line. For a length of 300 m, it consists of $R_{L1}=R_{L2}=10 \Omega$, $L_1=L_2=300 \mu H$ and, $C_L=50 nF$.
- To the right there is the module located in the receiver and it is in turn divided into three blocks: The operational amplifier (block reception), the capacitor C_3 and R_9 (block transmission), the power line formed by the 18 V and the shunt resistance of 47 Ω (from which the signal of measurement is obtained).

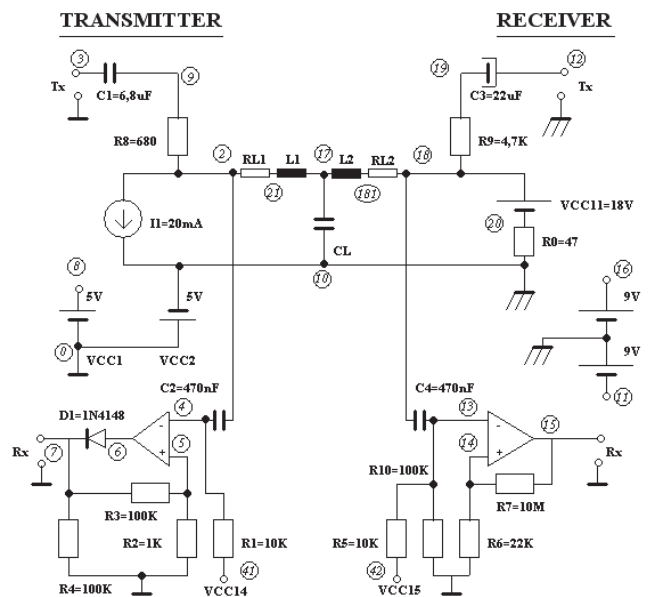


Fig. 7. Digital communication system.

III. SIMULATION RESULTS

Fig. 8 shows the chronograms of a digital communication system simulation (circuit in Fig. 7), in a

communication from the transmitter to the receiver at 4800 bps with a 300-meter twisted pair line.

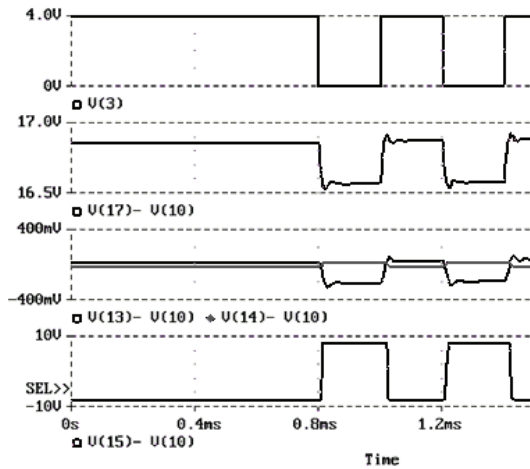


Fig. 8. Communication from transmitter to receiver with a 300-meter line.

Fig. 9 shows the chronograms of a digital communication system simulation (circuit in Fig. 7), in a communication from the receiver to the transmitter at 4800 bps with a 300-meter twisted pair line.

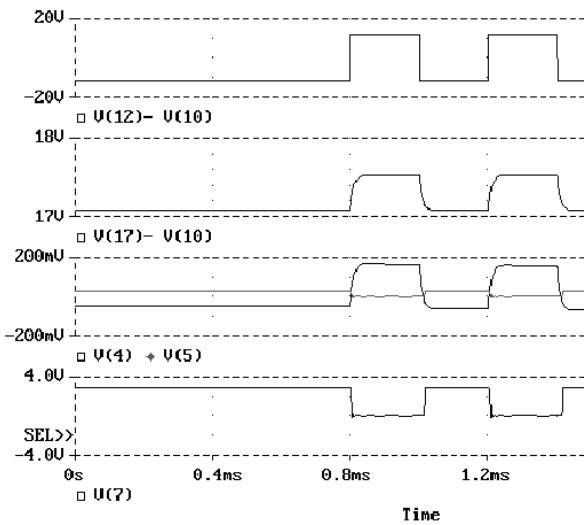


Fig. 9. Communication from receiver to transmitter with a 300-meter line.

The average value of the variation of intensity in the line, produced by the messages (asked/answered $V(17)-V(10)$ in Fig. 8 and Fig. 9) in a communication, must be equal to zero [7]. The signal $i(t)$ is the corresponding mixture of measurement signal (I_m) and digital communication signal. At every instant the integral of $i(t)$, between t_1 and t_2 , is equal to I_m (Fig. 10).

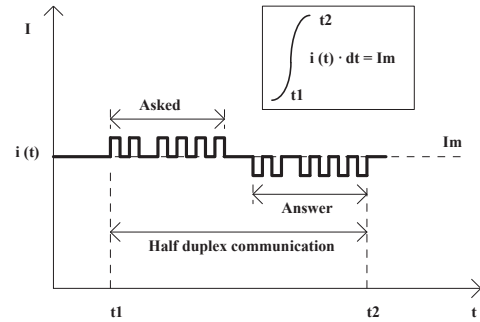


Fig. 10. Digital communication features.

IV. CONCLUSIONS

The transmitter that has been designed and developed meets perfectly the original specifications in functionality and performance. This can be an important benchmark in the design and manufacturing of transmitters with a higher degree of sophistication. Finally, Table 1 shows some of the most important characteristics of the transmitter.

ACKNOWLEDGMENT

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TABLE 1. DESIGNED ITF FEATURES.

Signal of measurement: J, K, S, T, R, B, E, Pt-100, 100mV, 1V and 0/4...20 mA.
Linearization within the response curves of thermocouples.
Automatic cold junction compensation and correction of drift time.
Software for the adjustment channels measurement.
Software for changing scales and ranges of work.
Communication: 4800 or 9600 baud
Maximum distance between transmitter and receiver: 300 meters.
Powered by the connection wires.
Analogical signal compatible with digital communication.

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