This project tries to develop totally one circuit, in our case a four multiplexed displays display, begining from the initial idea in blocks, one possible pre-design with comercial integrated circuits (making a very huge circuit), and the evolution to the interpreted language VHDL, wich leave us ‘program’ the needed functions, in only three SPLDs in our case, and test the result simulating first and implementing the resultant design later on.

Is a project with academycal intentions to show the evolution of some technologies and it’s use, and introducing the design tools of schematics capture and language VHDL interpretation: OrCAD Capture, and of mixed analogic and digital circuits simulation for it’s record in programable chips: OrCAD Simulate.

Furthermore the final design includes the abstract circuit assembling in a tangible printed circuit with OrCAD LAyout.