Standalone Self-Excited Induction Generator with a Three-Phase Four-Wire Active Filter and Energy Storage System

José Antonio Barrado
DEEEA-ETSE
Universitat Rovira i Virgili
Tarragona, Spain
Email: joseantonio.barrado@urv.cat

Robert Griñó
IOC-ETSEIB
Universitat Politècnica de Catalunya
Barcelona, Spain
Email: roberto.grino@upc.edu

Hugo Valderrama
DEEEA-ETSE
Universitat Rovira i Virgili
Tarragona, Spain
Email: hugo.valderrama@urv.cat

Abstract—This paper proposes a system, based on a self-excited induction generator with a shunt electronic converter, to feed isolated three-phase and single-phase linear or nonlinear loads. The electronic converter is composed by a three-phase four-wire voltage source inverter (VSI) and, connected to its dc side, two dc/dc converters. The VSI compensates the current harmonics, the reactive power and the load unbalances. The first of the dc/dc converters is a battery charger/discharger and the other converter dissipates the active power excess, through a chopper, so these dc/dc converters keep the active power balance of the complete system. The designed control architecture assures that, in steady state, the rms values of the voltages and the frequency remain at the their reference values. The simulated results show a good performance of the system under different loading conditions.

I. INTRODUCTION

The self-excited induction generator (SEIG) has some advantages over the synchronous generator, for example: brushless (squirrel-cage rotor), reduced size, rugged and low cost. However, the induction generator offers poor voltage regulation and its value depends on the prime mover speed, capacitor bank size and load characteristics [1].

The cage-type induction generators advantages have led to choose them as a suitable candidate for energy supply in rural communities and remote areas, where these machines can be driven using some renewable energy resources like wind energy or micro-hydropower.

When a three-phase four-wire SEIG feeds unbalanced and/or non-linear loads, the terminal voltage and stator currents are also unbalanced and may appear current and voltage harmonics that increase the power losses, create unequal heating and cause torque pulsation on the shaft of the generator. Besides this, unbalanced three-phase currents yield a current in the neutral conductor that causes more power losses and heating.

These problems can be solved using different electronic converter topologies and control architectures. This work proposes, as an improvement for the system studied in [2], to add a bidirectional converter between the active filter dc bus and a battery set to provide energy storage capability to the whole system. In this way, the multi-converter system can compensate the abovementioned effects and transfer the suitable value of power in either direction between the battery and its ac terminals. So, the whole system increases its efficiency and capability.

II. SYSTEM DESCRIPTION

The system under study consists of a standalone SEIG driven by a prime mover. The generator supplies a three-phase four-wire system containing any kind of load. The voltage and frequency control is achieved by means of an electronic converter connected to the generator terminals with the necessary control loops to regulate these electrical variables (Fig. 1).

The SEIG is a three-phase induction machine with a wye-connected stator winding with its middle point connected to the system’s neutral conductor. A three-phase shunt capacitor bank supplies a constant reactive power to the SEIG. Finally, a prime mover drives the squirrel-cage rotor at a speed higher than the synchronous one.

The electronic converter consists of a shunt active filter (AF) made up of a four-leg voltage source inverter (VSI) with a split-capacitor dc bus [3] and two dc/dc converters connected to this dc bus.

Fig. 1. Schematic diagram of three-phase four-wire system.
One of this is an electronic load controller (ELC), made up of a chopper connected to a resistive load [4], and the other is a bidirectional current battery converter (BC) [5] which acts as a buck converter in the charging phase and as a boost converter in the discharging phase. This converter allows the battery to storage or to supply energy from or to the ac side of the system.

The proposed control architecture allows to the compound converter to eliminate current harmonics, to compensate reactive power and to balance the load at the SEIG terminals, as well as to storage or to inject energy depending on the ac load needs.

III. MATHEMATICAL MODEL OF SYSTEM

A. Induction Generator Model

The following equations describe the dynamic model of the squirrel-cage induction generator in the stationary dq0 reference frame [6].

The flux linkages per second taken into account the saturation effect are

\[
\psi_{qs} = \omega_b \int \left( v_{qs} + \frac{r}{x_{ls}} (\psi_{sat} - \psi_{qs}) \right) dt \tag{1}
\]

\[
\psi_{ds} = \omega_b \int \left( v_{ds} + \frac{r}{x_{ls}} (\psi_{sat} - \psi_{ds}) \right) dt \tag{2}
\]

\[
\psi'_{qr} = \omega_b \int \left( \frac{\psi_{dr}}{\omega_b} \psi'_{dr} + \frac{r'}{x'_{lr}} (\psi_{sat} - \psi'_{qr}) \right) dt \tag{3}
\]

\[
\psi'_{dr} = \omega_b \int \left( -\frac{\psi_{dr}}{\omega_b} \psi'_{dr} + \frac{r'}{x'_{lr}} (\psi_{sat} - \psi'_{dr}) \right) dt \tag{4}
\]

The stator qd0 currents can be calculated as

\[
i_{qs} = \frac{\psi_{qs} - \psi_{sat}}{x_{ls}} \tag{5}
\]

\[
i_{ds} = \frac{\psi_{ds} - \psi_{sat}}{x_{ls}} \tag{6}
\]

\[
i_{0a} = \frac{\psi_{0a}}{x_{ls}} \left( v_{0a} - r_i l_{0a} \right) dt \tag{7}
\]

The electromagnetic torque equation is

\[
T_{em} = \frac{3 P}{2} \frac{d}{dt} \left( \psi_{ds} i_{qs} - \psi_{qs} i_{ds} \right) \tag{8}
\]

The rotor motion is given by

\[
\frac{d}{dt} \frac{d\omega}{dt} + T_{damp} - T_{mec} \tag{9}
\]

The magnetic saturation is the main factor for voltage buildup and its stabilization on SEIG. For machines with uniform air gap, the saturated value of the mutual flux linkage per second in qd-axes is given by

\[
\psi_{sat}^m = x_M \left( \frac{\psi_{qs} + \psi_{qr} - \Delta \psi_{mq}}{x_{ls}} \right) \tag{10}
\]

\[
\psi_{sat}^d = x_M \left( \frac{\psi_{ds} + \psi_{dr} - \Delta \psi_{md}}{x_{ls}} \right) \tag{11}
\]

Where

\[
\frac{1}{x_M} = \frac{1}{x_{ls}} + \frac{1}{x'_{lr}} + \frac{1}{x_{m}} \tag{12}
\]

Assuming a proportional reduction in the mutual flux linkages of the qd-axes, the value of $\Delta \psi_{mq}$ and $\Delta \psi_{md}$ is determined from the following relationships

\[
\frac{\psi_{mq}^m}{\psi_{sat}^m} = \Delta \psi_{mq} ; \frac{\psi_{md}^d}{\psi_{sat}^d} = \Delta \psi_{md} \tag{13}
\]

Where the values of $\Delta \psi_{mq}$ and $\psi_{sat}^m$ are obtained from the terminal voltage versus no-load stator current curve.

The model of induction generator, with saturated mutual flux linkage per second, is implemented with Simulink. It is divided in blocks that consider the transformations: abc-qd0 of the stator voltages and qd0-abc of the stator currents, and solve the previous equations (1) to (13): stator currents, flux linkages, magnetic saturation, torque and rotor speed [7].

B. Electronic Converter Model

Fig. 2 shows the electrical scheme of the electronic converter. The ac side of this system is made up of a three-phase four-wire current controlled voltage source inverter (CC-VSI), acting as active filter [8]. The three-leg converter topology with the neutral wire connected directly to the mid point of the dc bus capacitors (split-capacitor dc bus) is preferred due to its lower number of power semiconductor devices with regard to the four-leg topology.

The CC-VSI is modeled by three switching functions (SA, SB, and SC) that determine the state of the IGBTs (SAP to SCN) in each branch of the bridge. Considering the commutation basic rules of converters, the switches of a branch must change in a complementary form.

As far as the dc/dc converters are concerned, the first one is a bidirectional converter for charging/discharging a battery. The two IGBTs of this battery converter are switching, in a complementary form, by means of SD function. This function is a PWM signal and its duty cycle value determines the battery current direction and magnitude. The second converter is a chopper, who switches a resistive load, and its only IGBT is switching by SE function.

The differential equations in each ac terminal of the converter, according to its respective switching functions, are

\[
v_{xa} = R_{xa} i_{xa} + L_{ia} \frac{di_{xa}}{dt} + e_{xa} , \quad x = \{a,b,c\} \tag{14}
\]

\[
e_{xa} = S_y v_{C1} - S_y v_{C2} , \quad x = \{a,b,c\}, \quad y = \{A,B,C\} \tag{15}
\]
The ac side currents of CC-VSI must follow their reference currents. For this purpose, three independent hysteretic current controllers working in closed loop generate the switching patterns for each branch of the converter.

The voltage of dc bus is calculated from the inductor currents at the VSI and the currents at dc/dc converters as follows

\[ v_{dc} = v_{c1} + v_{c2} \]  
\[ v_{c1} = \frac{1}{C_1} \int (S_A i_a + S_B i_b + S_C i_c - S_D i_{BC} - S_E i_{Rdc}) \, dt \]  
\[ v_{c2} = \frac{1}{C_2} \int (S_A i_a + S_B i_b + S_C i_c - S_D i_{BC} - S_E i_{Rdc}) \, dt \]

Due to the boost-type operation of the inverter, the \( V_{dc} \) set point must be greater than two times the peak value of ac phase-to-neutral voltages in order to obtain the desired operation.

The dc side currents of the VSI and its relationships with the ac side currents are expressed by

\[ i_{dc} = i_{c1} + i_{dc} \]  
\[ i_{c2} = i_{c1} + i_n \]  
\[ i_a + i_b + i_c + i_n = 0 \]

The currents through the dc/dc converters are

\[ i_{dc} = i_{BC} + i_{Rdc} \]  
\[ i_{BC} = S_D i_{L1} \]  
\[ i_{Rdc} = S_E \frac{v_{dc}}{R_{dc}} \]

The differential equations of the battery converter are as follows

\[ u_1 = S_D v_{dc} \]  
\[ u_1 - v_{C3} = R_{L1} i_{L1} + L_{L1} \frac{di_{L1}}{dt} \]  
\[ v_{C3} = \frac{1}{C_3} \int (i_{L1} - i_{Batt}) \, dt \]  
\[ v_{C3} - v_{Batt} = R_{L2} i_{Batt} + L_2 \frac{di_{Batt}}{dt} \]

The chosen LCL topology for the filter allows a higher attenuation of the high frequency harmonic content in the injected current to the battery than the usual L filter.

IV. CONTROLLER DESCRIPTION

Fig. 3 shows the block diagram of the proposed control architecture. The controller forces the electronic converter to compensate the load current and to absorb a determinate value of active power from the system. Thus, the current supply from SEIG becomes sinusoidal, balanced and the values of voltage and frequency are maintained constant.

Therefore, the induction generator provides the active power demanded by the load and the other active power necessary to keep the voltage in the dc capacitors. This additional component determines a current value to cover the losses in the power converter (switching losses of IGBTs, leakage current of capacitors, etc), to supply the current of controlled dc load and to charge a battery. All this power changes cause voltage variations on the dc-link [9] that are corrected by the dc voltage regulator.

The main block of the proposed control is a three-phase PLL structure. A first PLL determines the fundamental frequency.
and the synchronizing signals from the ac voltage. Another block has two three-phase PLL for extracting the magnitude and phase angle of fundamental positive-sequence of the voltage line and current load, respectively [10] [11].

From these two magnitudes and its phase difference, the block “magnitude to abc” generates the three-phase balanced active component of the load current, \(i_{La}(abc)\). To remark that these signals are sinusoidal and keep 120° among themselves, they have equal amplitude and its frequency value is equal to the fundamental one.

The difference between the generated value, \(i_{La}(abc)\), and the measured load current, \(i_L(abc)\), yields the reference current to compensate the reactive power, the unbalanced currents and harmonics of the three-phase four-wire general load [12].

The total reference current to apply on CC-VSI block, \(i_C(abc)^*\), are the sum of the above component (compensated load current) and two other components: the signal \(i_{CP}(abc)\) and the signal \(i_{CQ}(abc)\).

The reference \(i_{CP}(abc)\) is the necessary active current to balance the active power in the whole system in order to maintain the desired frequency (frequency regulator) and to compensate the losses in the electronic converter (dc voltage regulator).

Therefore, the frequency regulator supplies an ac current magnitude in phase with ac voltage signals. These three-phase magnitudes yield an active power value that is equivalent to dc power. It is divided by the voltage value on dc bus, thus generating the reference current, \(i_{C_{dc}}\), for dc/dc converters. When the SEIG is working without ac load, this reference includes the battery current value (equivalent to 40% of the SEIG rated current) and the reference to the controlled dc load (corresponding to the remaining of the active power excess).

When an ac load is switched on to the SEIG the demand of generating power is increased; this yields a reduction in the rotor speed and, consequently, of the frequency value. So, the lower output signal of the frequency regulator forces to

The reference \(i_{C}(abc)^*\) is the necessary active current to balance the active power in the whole system in order to maintain the desired frequency (frequency regulator) and to compensate the losses in the electronic converter (dc voltage regulator).

Therefore, the frequency regulator supplies an ac current magnitude in phase with ac voltage signals. These three-phase magnitudes yield an active power value that is equivalent to dc power. It is divided by the voltage value on dc bus, thus generating the reference current, \(i_{C_{dc}}\), for dc/dc converters. When the SEIG is working without ac load, this reference includes the battery current value (equivalent to 40% of the SEIG rated current) and the reference to the controlled dc load (corresponding to the remaining of the active power excess).

When an ac load is switched on to the SEIG the demand of generating power is increased; this yields a reduction in the rotor speed and, consequently, of the frequency value. So, the lower output signal of the frequency regulator forces to

Fig. 3. Block diagram of the electronic converter control.
decrease the current through the dc/dc converters. First, it decreases the current corresponding to the power dissipated by the chopper and the battery keeps its value of charge current. But if this behavior is not enough the bidirectional converter decreases the battery charge current to a suitable value. If the ac load is higher, then this last converter forces the battery to inject a determinate value of current to the ac side of the system, through to the VSI.

Besides this, the slower feedback control loop of the dc voltage regulator gives another reference signal, i_{loss}, to make the active filter to absorb an in-phase fundamental current to compensate the above mentioned voltage variation on dc side.

On the other hand, the signal i_{q}^{\text{abc}}(abc) is a reference current that sets the required reactive power for compensate the ac voltage variations on standalone induction generator. This reactive power is supplied by means of the electronic converter and the capacitor bank.

The ac voltage regulator calculates the above mentioned current component. It is important to emphasize that this current reference is in quadrature with the sensed three-phase voltage.

The three phase currents of the converter are controlled using a hysteretic controller for each branch (hysteresis band current control). Each controller acts over the corresponding difference between the reference currents of the converter, i_{c}^{(a,b,c)*}, and the measured ac side currents, i_{c}^{(a,b,c)}. Its operation determines the gating signals to switch the respective IGBT of the VSI.

The closed loop control of the battery converter is obtained from a PWM current controller. The relation Vbatt/Vdc and the signal from the battery regulator establish the duty cycle of the gating signal S_{B}. This value determines the state of the respective two IGBTs and consequently the magnitude and direction of battery current. Finally, the signal of the Rc current regulator is connected to the input of another PWM current controller. The output of this controller determines the gating signal, S_{E}, to switch the IGBT of the chopper. Thus, these two dc/dc converters and its controllers regulate the flow of active power in order to balance the active power of the whole system.

V. SIMULATION RESULTS

This section shows the performance of the model by means some simulation results of the controlled system.

Fig. 4 shows the response of the system when a dynamic three-phase load is connected. A dynamic balanced three-phase load is connected to the system. This action implies heavy variations of the ac and dc magnitudes on the SEIG and the electronic converter. However, the controller responds driving the magnitude and frequency of generated voltage to their reference values.

Fig. 4 also shows that the battery first stores and then injects a determinate value of current by means the controlled bidirectional battery converter. Consequently, the active power flows as a function of the ac load conditions. This possibility allows increasing the efficiency and availability of the proposed generating system.

The left side of Fig. 4 depicts the respective values of the ac and dc currents in the electronic converter.

At time 1 second, a dynamic balanced three-phase load is connected to the system. This action implies heavy variations of the ac and dc magnitudes on the SEIG and the electronic converter. However, the controller responds driving the magnitude and frequency of generated voltage to their reference values.

Fig. 4 also shows that the battery first stores and then injects a determinate value of current by means the controlled bidirectional battery converter. Consequently, the active power flows as a function of the ac load conditions. This possibility allows increasing the efficiency and availability of the proposed generating system.

Fig. 5 shows the behavior of the complete system with a single-phase inductive load and a non-linear load (three-phase rectifier bridge with a capacitive filter and a resistive load). The AF block of the converter compensates the neutral current and current harmonics that are due to the unbalanced and nonlinear nature of the ac load.
loops allow to maintain the rated voltage and frequency under different loading conditions and increasing the efficiency and availability of system. The simulation results show a good performance and the efficiency of the whole system under different loading conditions.

## APPENDIX

The values of the parameters used to obtain the simulation results are:

- Induction machine and capacitor bank:
  \[ P = 4 \text{ kW}, \ V_{ac} = 220 \text{ V}, \ f = 50 \text{ Hz}, \ 4 \text{ poles} \]
  \[ R_s = 1.365 \Omega, \ R_r = 1.405 \Omega, \ L_1 = 5.839 \text{ mH} \]
  \[ L_m = 172.2 \text{ mH}, \ J = 0.013 \text{ kg.m}^2, \ C_{ac} = 3 \times 60 \text{ mF} \]

- Electronic converter and battery:
  \[ C_1 = C_2 = 3000 \text{ mF}, \ C_3 = 4.7 \text{ mF} \]
  \[ L_1 = 3 \text{ mH}, \ L_2 = 0.8 \text{ mH}, \ L_3 = 5 \text{ mH} \]
  \[ R_{dc} = 72 \Omega, \ V_{batt} = 300 \text{ V} \]

### ACKNOWLEDGMENT

This work has been partially sponsored by the Spanish "Ministerio de Educación y Ciencia" under the grant DPI2003-0887-C03-C02.

### REFERENCES


---

**Fig. 5.** Steady state waveforms of system supplying an unbalanced and non-linear load.