Reduction of the common mode voltage of a matrix converter fed direct torque control

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Abstract: The work presented in this paper proposes an approach to reduce the common mode voltage at the output of matrix converters when a direct torque control scheme is employed to drive a permanent magnet synchronous motor. A mathematical analysis of the common mode voltage at the output of matrix converters is firstly carried out which is then corroborated by simulation results. Based on this analysis, a very simple algorithm to reduce the common mode voltage is proposed and investigated. Finally, experimental results corroborating the effectiveness of the proposed method are shown.

Keywords: matrix converters, common mode voltage, direct torque control

Classification: Electromagnetic compatibility (EMC)

References

1 Introduction

The Common Mode Voltage (CMV) produced by modern power converters is considered as one of the main sources of early motor winding failure and bearing deterioration. High frequency components and large amplitudes of the CMV at the motor neutral point have been shown to generate high frequency currents to the ground path and induced shaft voltage [1]. Strategies to reduce the CMV at the output of standard Voltage Source Inverter (VSI) have been proposed in the literature [2]. In the recent past, Matrix Converters (MCs) [3] have received considerable attention as they are becoming a good alternative to the VSI. In [4], a new modulation strategy of the MC which reduces the CMV is presented. Based on the Indirect Space Vector Modulation (ISVM) technique, the method selects a medium value phase voltage as a zero vector and places it in the center or on the both sides of the sampling period without changing the active voltage vectors. In [5], the use of a MC to implement a Direct Torque Control (DTC) of an Induction Motor (IM) was introduced.

In the last decade, DTC has emerged to become an attractive alternative to the well-known Field Oriented Control (FOC). A very good performance, obtaining results as good as the classical FOC but with several advantages based on its simpler structure and control diagram, is one of its main characteristics.

This paper analyzes the CMV at the output of MCs. The mathematical analysis is confirmed by simulation results. Based on this analysis and the approach presented in [4], an algorithm to reduce the CMV when DTC is employed to drive a Permanent Magnet Synchronous Motor (PMSM) is developed. The algorithm is based on the selection of proper zero vectors without altering the original DTC using MC described in [5]. The effectiveness of the method is validated by experimental results which, in contrast to those obtained in [4], show that the CMV do not depend on the output frequency.

2 Common Mode Voltage in Matrix Converters

A MC connected to a PMSM is shown in Fig. 1. The impedance $Z_{cm}$ represents the leakage current path between the machine’s neutral point ($n$) and ground ($N$).

The CMV is defined as the voltage between these two points:

$$v_{cm} = v_{n,N}$$

(1)
Taking into consideration the CMV $v_{cm}$, the voltage expressions of the PMSM can be written as:

\begin{align}
    v_{aN} - v_{cm} &= R_s i_a + L_s \frac{di_a}{dt} + \frac{d}{dt} \psi_a \\
    v_{bN} - v_{cm} &= R_s i_b + L_s \frac{di_b}{dt} + \frac{d}{dt} \psi_b \\
    v_{cN} - v_{cm} &= R_s i_c + L_s \frac{di_c}{dt} + \frac{d}{dt} \psi_c 
\end{align}

where $v_{aN}$, $v_{bN}$, and $v_{cN}$ are the MC output voltages with respect to ground ($N$). $R_s$ and $L_s$ are the resistance and inductance of each phase of the PMSM respectively. Assuming a balanced induced voltage system $\psi_a + \psi_b + \psi_c \approx 0$ and the fact that $i_a + i_b + i_c \approx 0$, the CMV at the output of the MC can be found by adding (2), (3) and (4):

\[ v_{cm} = \frac{v_{aN} + v_{bN} + v_{cN}}{3} \]  

When active vectors are selected in a MC, there are two output phases connected to the same input phase [6]. Thus, the CMV when active vectors are delivered by a MC can be written as:

\[ v_{cm}^a = \frac{v_i + 2v_j}{3} \]

where $i$ and $j$ represent the two input phases involved when an active vector ($superscript a$) is delivered by the MC.

When the input filter is properly designed, the phase voltages at the input side of the MC $v_A$, $v_B$, and $v_C$ can be considered equal to the phase voltages of the grid $v_{SA}$, $v_{SB}$, and $v_{SC}$ respectively [6]. Assuming a balanced voltage at the grid and, hence, at the input side of the MC,

\[ v_{SA} = v_A = V_p \sin(\omega t) \]
where \( V_p \) is the voltage peak value per phase, (6) can be written as:

\[
v_{cm}^a = \frac{1}{3} V_p \sin (\omega t) + \frac{2}{3} V_p \sin (\omega t \pm \frac{2\pi}{3})
\]  

(10)

Rearranging (10), a simpler form of \( v_{cm}^a \) is obtained:

\[
v_{cm}^a = \frac{1}{\sqrt{3}} V_p \sin (\omega t \pm \frac{\pi}{2})
\]  

(11)

Equation (11) states that the instantaneous value of the CMV \( v_{cm}^a \), when an active vector is delivered by the MC depends on the instantaneous value of the input voltages selected to deliver such vector. The maximum instantaneous value of the CMV \( v_{cm}^a \), when active vectors are delivered by the MC will be:

\[
v_{cm}^a_{\text{max}} = \pm \frac{1}{\sqrt{3}} V_p
\]  

(12)

The CMV will reach its maximum value \( v_{cm}^a_{\text{max}} \) every \( \pm \kappa \pi \) radians.

Each active vector contributes to the CMV as shown in Fig. 2(a) and (b), where \( v_{SA}, v_{SB}, \) and \( v_{SC} \) are the grid voltages with a frequency of 50 Hz. During each input voltage period (\( T = 0.02 \) s), a different output vector is delivered by the MC as shown in Fig. 2(a) and Fig. 2(b), where \( \pm 1 \ldots \pm 9 \) are the MC active vectors as defined in [5]. As it can be seen, for every active vector, the CMV is at its maximum value, \( \pm \frac{1}{\sqrt{3}} V_p \), in every \( \pm \kappa \pi \) radians.

When a zero vector is selected in a MC, the three output phases are connected to the same input phase. Thus, when a zero vector is delivered by a MC, the CMV \( v_{cm}^z \) will be equal to the selected input voltage:

\[
v_{cm}^z = v_i
\]  

(13)

where \( i \) denotes the selected input phase.

Fig. 2. Common mode voltage. (a) and (b) when active vectors are delivered by a MC. (c) when zero vector are delivered by the MC. (d) Selecting the phase voltage with minimum absolute value.
Since \( v_i = V_p \sin (wt \pm \phi) \), the maximum CMV \( v_{cm}^z \) when a zero is selected will be:

\[
v_{cm_{max}}^z = \pm V_p
\]  

(14)

The contribution to the CMV of each zero vector is shown in Fig. 2 (c), where a different zero vector is selected in every input voltage period \((T = 0.02 \text{s})\). It can be noted how the CMV is equal to the amplitude of the selected input phase to deliver the zero vector, being the maximum value of the CMV equal to the peak value of that phase.

### 3 Reduction of the Common mode Voltage

In DTC schemes, a zero vector is usually selected when the actual electromagnetic torque is within the corresponding comparator hysteresis bands [5]. In a MC, a zero vector is delivered by connecting all the output phases to the same input phase. Since there are three input phases supplying the MC, three different zero vectors can be delivered depending on which input phase is connected to the three output phases. This degree of freedom can be exploited to reduce the CMV by proper selection of the zero vectors.

When an input phase voltage reaches its positive or negative peak value \( \pm V_p \), the other two phases will be at \( \pm \frac{1}{2}V_p \). Hence, the maximum CMV can be reduced to \( \pm \frac{1}{2}V_p \) if the input phase voltage with minimum absolute value is selected to deliver the required zero vector. The contribution to the CMV of zero vectors when the input phase with minimum absolute value is selected to deliver a zero vector is shown in Fig. 2 (d). When a zero vector is required by the DTC algorithm, the system computes the absolute value of every input phase and selects the one with the minimum value. The selected phase will be the one connected to the three output phases to deliver the zero vector. In contrast with the ISVM described in [4], where the selection of zero vectors with the minimum absolute value can not always be ensured due to the varying nature of the input voltage and the longer time during which zero vectors are applied at low output frequency, the proposed method ensures the selection of the most suitable zero vector irrespective of the output frequency.

### 4 Experimental Results

In order to validate the theoretical analysis, experimental tests have been carried out employing a PMSM with the following parameters: number of pole pairs \( p = 4 \), power \( P = 200 \text{ W} \), torque \( T_e = 0.64 \text{ Nm} \), speed \( N = 3000 \text{ rpm} \), permanent magnet flux \( \psi_{PM} = 0.046 \text{ Wb} \), quadrature axis impedance \( L_q = 8.6 \text{ mH} \). The proposed method to reduce the CMV is analyzed and compared with the classical DTC using MC proposed in [5], in which the reduction of the number of switching is used as the criteria to select zero vectors. The DTC settings in both experiments are: \( T_e^* = 0.64 \text{ Nm} \) (rated torque), \( |\psi_s^*| = \sqrt{\left|\psi_{PM}^2 + \left(\frac{3}{2} \frac{T_e^* L_q}{3 \psi_{PM}}\right)^2\right|} \), \( BT_e = \pm 0.015 \text{ Nm} \), \( B\psi_s = \pm 0.0006 \text{ Wb} \), where \( T_e^* \) is the torque reference, \( |\psi_s^*| \) is the flux reference, and \( BT_e \) and \( B\psi_s \) are the torque and flux hysteresis bands respectively.
The CMV when the classical DTC using MC is used to drive the PMSM is shown in Fig. 3 (a). The maximum CMV value $v_{cm_{max}}$ reaches ±100 V, corresponding to the input phase negative and positive peak values. As it has been explained in section 2, this occurs when an input phase crossing its peak value is selected to deliver a zero vector.

When the proposed method is used under the same conditions, the maximum CMV value is reduced to ±57.7 V, as shown in Fig. 3 (b), corresponding to $±1/\sqrt{3}$ of the input phase peak value when active vectors are selected.

In addition to a reduction of the motor winding failure and degradation of its bearings due to the reduction in the CMV [2], comparing the harmonic spectrum of $v_{cm}$ shown in Fig. 3 (a) and Fig. 3 (b), it can be said that the better performance shown by the proposed method will reduce the motor shaft induced voltage [1].

Several experimental tests, covering all the machine operating points, have been carried out in order to deeply compare both methods. The speed has been varied from 100 rpm up to 3000 rpm (rated speed), whereas the torque reference has been varied from 0.1 Nm up to 0.64 Nm (rated torque). In order to assess the performance of both systems, only the RMS value of the CMV $v_{cm_{RMS}}$ has been considered since its peak value is expected to
remain constant irrespective of the operating conditions.

The results, comparing the classical and the proposed method, are shown in Fig. 3 (c) and Fig. 3 (d) respectively. It is noted that the better performance of the proposed method were verified at overall the operating points. Moreover, in contrast with the results obtained in [4], the flatness of both figures denotes that the RMS value of the CMV $v_{cm,RMS}$ does not depend neither on torque nor on speed.

5 Conclusion

The common mode voltage produced by MCs has been analyzed focusing on the DTC driving PMSMs. The contribution of zero vectors to the CMV has been shown to be greater than that of active vectors. The advantages of the MCs, such as the availability to deliver three different zero vectors, have been exploited to reduce the CMV in DTC drives. A very simple and effective zero vector selector has been developed allowing a reduction of the maximum CMV in 42.3% while not deteriorating the output harmonic content. Moreover, the better harmonic spectrum of the CMV, specially at high frequencies, indicates that there will be a lower level of common mode current contributing to extend the machine’s operational life.